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Analysis and Comparison Based on Component Stress Factor of Dual Active Bridge and Isolated Full Bridge Boost Converters for Bidirectional Fuel Cells Systems

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Abstract—This paper presents an analysis and comparison of isolated topologies for bidirectional fuel cell systems. The analyzed topologies are the dual active bridge (DAB) and the isolated full bridge boost converter (IFBBC). The analysis is performed based on the component stress factor (CSF). Results highlight that the DAB has lower CSF than the IFBBC for narrow converter operating points. On the other hand the IFBBC presents a more homogeneous CSF over the entire converter operating range. Finally, experimental results obtained from a 30-80 V 80 A 6 kW 40 kHz IFBBC are presented. The converter achieves efficiencies up to 98.2% and 97.45% depending on the converter power flow.

Keywords—Dual Active Bridge (DAB), Isolated full Bridge Boost Converter (IFBBC), Component Stress Factor (CSF), High Efficiency, Bidirectional Fuel Cells.

I. INTRODUCTION

Power converter's efficiency has been one of the major driving forces in power electronics especially over the last two decades. The latest developments in the power semiconductor technologies allowed developing converters with efficiency above 98% even with isolated topologies [1],[2]. However, selecting the latest silicon carbide (SiC) power semiconductors [3] it is not sufficient to achieve high efficiency. It is necessary to perform an analysis and optimization of different solutions in terms of converter topology, power semiconductors, magnetic component design as well as converter optimization.

Large scale integration of renewable energies requires grid tie energy storage to balance the energy production and consumption [4]. For these applications bidirectional fuel cells represent an attractive technology [5]. However, bidirectional fuel cells, also called regenerative or reversible fuel cells (RFCs), have wider operating conditions than conventional unidirectional fuel cells. Choosing the most suitable topology that can guarantee good performance over the entire system operating range is always troublesome and challenging. This is especially true for fuel cells applications where power converters are expected to operate at low-voltage and high-current levels.

This paper presents an analysis of two isolated dc-dc converter topologies: the isolated full bridge boost converter (IFBBC) [2] and the dual active bridge (DAB) [6]. The two topologies are analyzed in terms of component stress factor [7],[8] and compared based on the converter specifications for bidirectional fuel cell applications. Based on the analysis, a 6 kW bidirectional dc-dc IFBBC has been developed. The converter prototype achieves peak efficiencies of 98.2% and 97.45% depending on the converter power flow.

II. SYSTEM ANALYSIS AND SPECIFICATIONS

An energy storage bidirectional system based on regenerative fuel cells requires high efficiency dc-dc and dc-ac converters to effectively operate the system at its maximum performance. The cell technology strongly affects the system topology in fact, the voltage of a single cell is too low to realized multi-kW energy storage systems. It is necessary to stack several cells to achieve voltage levels that can better be processed by multi-kW power converters. Stacking a large number of cell increases the manufacturing challenges, such as homogeneous operating conditions of the cells stack and fuel pressure equalization. Moreover, not all fuel cells are suitable for bidirectional operation since operating in the two modes could significantly change the stress conditions of the cells and affect their reliability.

Based on a close cooperation with a large fuel cells manufacturer, it was determined that new solid oxide cell technology can be operated in both fuel cell mode (SOFC) and electrolyzer cell mode (SOEC). Table I shows the dc-dc converter specifications, which are defined based on a laboratory prototype of SOFC/SOEC cells stack. A 50 kW

TABLE I
SOFC AND SOEC DC-DC CONVERTER SPECIFICATIONS

	SOFC	SOEC
Low Voltage (LV) side	30-50 V	50-80 V
Current (LV) side	40-0 A	0-80 A
High Voltage (HV) side	700-800 V	700-800 V
Power Rating	~1500 W	~6000 W

Fig. 1 IFBBC topology.

system for grid connected energy storage applications is presented in [9]. The system is composed by 10 cells stacks and each stack has a dc-dc converter. The operating mode of the system (SOFC or SOEC) determines the operating conditions of the dc-dc converter (I-V curve, [9]) and therefore, the system efficiency for the different operating points.

III. ISOLATED TOPOLOGIES FOR BIDIRECTIONAL FUEL CELLS SYSTEMS: SELECTION AND ANALYSIS

Different isolated topologies can be candidate for energy storage systems based on bidirectional fuel cells. However, resonant topologies have difficulties to achieve suitable high efficiency when the converter ports voltage varies significantly (e.g. in this application by a factor 2.6). Another factor that influences the selection of the topology is its complexity in terms of number of power semiconductors, passive components and control. All these parameters will significantly affect also the cost of the converter and the economical sustainability of the system.

The results from the analysis of different isolated dc-dc converter topologies highlighted two candidate topologies suitable for the system: the isolated full bridge boost converter (IFBBC) and the dual active bridge (DAB).

A. Isolated Full Bridge Boost Converter (IFBBC)

The IFBBC topology shown on Fig. 1, has proved to achieve efficiency up to 98% [2] in fuel cell applications. The main CCM operating waveforms are presented in Fig. 2. The boost inductor L_{BOOST} is the component that controls the energy transfer from the converter low voltage side to the high voltage side and it has to handle the high current level I_{LV} on the converter low voltage side. Given an operating point, the duty cycle ($0.5 < D < 1$) is calculated according to (1).

$$D = 1 - \frac{nV_{LV}}{2V_{HV}} \quad (1)$$

For a given converter switching frequency $f_{sw}=1/T_{sw}$, the maximum and minimum boost inductor currents are expressed by (2) and (3) respectively. The transformer rms current of the low voltage side (4) can be calculated using (2) and (3).

$$i_0 = I_{LV} - \frac{V_{LV}T_{sw}(2D-1)}{4L} \quad (2)$$

$$i_1 = I_{LV} + \frac{V_{LV}T_{sw}(2D-1)}{4L} \quad (3)$$

Fig. 2 IFBBC CCM main operating waveforms.

$$I_{tr,LV,rms} = \frac{\sqrt{2}}{\sqrt{3}} \sqrt{(1-D)(i_0^2 + i_1^2 + i_0 i_1)} \quad (4)$$

By neglecting the ripple in the boost inductor, it is possible to simplify (4) obtaining the transformer current in the low voltage winding as shown in (5).

$$I_{tr,LV,rms} = I_{LV} \sqrt{2(1-D)} \quad (5)$$

Assuming ideal current distribution in the converter switches, during the intervals t_0 - t_1 and t_2 - t_3 each switch on the converter low voltage side carries $I_{LV}/2$. During the intervals t_1 - t_2 and t_3 - T_{sw} the full I_{LV} current flows through the switch. This results that the rms current of the low voltage side switches can be calculated as in (6).

$$I_{sw,LV,rms} = I_{LV} \sqrt{\frac{3}{4} - \frac{D}{2}} \quad (6)$$

The rms current of the high voltage side transformer winding and of the high voltage switches is expressed as a function of the current in the low voltage winding as in (7).

$$I_{tr,HV,rms} = \frac{I_{tr,LV,rms}}{n}; \quad I_{sw,HV,rms} = \frac{I_{tr,LV,rms}}{n\sqrt{2}}; \quad (7)$$

B. Dual Active Bridge (DAB)

In the DAB topology, shown in Fig. 3, the power flow is controlled through the ac-inductance in series with the transformer. Phase-shift modulation allows controlling the power flow in the converter with the phase-shift angle. By defining φ the phase-shift angle between the primary full bridge and the secondary full bridge, the power transfer between the low-voltage and the high voltage sides of the converter can be expressed as in (8). The maximum power transfer is achieved for $\varphi = \pi/2$; it can be observed that large

Fig. 3 DAB topology.

values of the L_{ac} would limit the maximum power flow in the converter.

$$P = \frac{V_{LV} V_{HV}}{n \omega L_{ac}} \varphi \left(1 - \frac{\varphi}{\pi}\right) \quad (8)$$

Defining the voltage transfer ratio as in (9) allows expressing the transformer current at time intervals t_0 and t_1 as in (10) and in (11) respectively.

$$M = \frac{V_{HV}}{n V_{LV}} \quad (9)$$

$$i_0 = \frac{-V_{LV}}{2\omega L_{ac}} [(1-M)(\pi - \varphi) + (1+M)\varphi] \quad (10)$$

$$i_1 = \frac{V_{LV}}{2\omega L_{ac}} [(1+M)\varphi + (1-M)(\pi - \varphi)] \quad (11)$$

The symmetry of the transformer current (12) allows expressing the rms transformer low voltage side current as in (13). The transformer current on the high voltage winding is related to the rms current on the transformer low voltage side by the turns ratio n . The ac-inductor current rms current is the same as the rms current of the transformer low voltage side winding. It is a common design procedure to integrate the ac-inductor in the transformer by tuning the transformer leakage inductance.

$$i_2 = -i_0; \quad i_3 = -i_1; \quad (12)$$

$$I_{tr.LV,rms} = \frac{1}{\sqrt{3}} \sqrt{i_0^2 + i_1^2 - i_0 i_1 + 2 \frac{\varphi}{\pi} i_0 i_1} \quad (13)$$

The rms current of both low voltage and high voltage switches is defined as a function of the transformer low voltage windings rms current as in (14).

$$I_{sw.LV,rms} = \frac{I_{tr.LV,rms}}{\sqrt{2}}; \quad I_{sw.HV,rms} = \frac{I_{tr.LV,rms}}{n\sqrt{2}}; \quad (14)$$

IV. COMPONENT STRESS FACTOR ANALYSIS

Component stress factor (CSF) is an analytical method used to evaluate and compare different converter topologies for a specific application. The method provides an estimation of the converter stresses and gives a quantitative measure of converter performance. The CSF method is similar to the component load factor (CLF) [4],[5], the difference in the two methods lies in how the individual and total components are calculated. In order to perform a fair comparison of the topologies, CSF assumes that the same amount silicon, magnetic material and capacitor volume are used on the analyzed topologies. This is

Fig. 4 DAB main operating waveforms with phase-shift modulation.

ensured by applying weighting factors to each component. The CSF analysis is performed based on three separate components: the semiconductor component stress factor (SCSF) (15), the winding component stress factor (WCSF) (16) and the capacitor component stress factor (CCSF) (17). The total stress over the different components is computed separately by adding together the relative components of the semiconductors (18), windings (19) and capacitors (20).

The different CSF values are calculated based on the devices voltages and rms currents. For power semiconductors the maximum voltage that the devices have to withstand over the entire converter operating range is considered. For wound components, such as inductors and transformers, V_{max} represents the maximum averaged value (based on duty cycle). For capacitive components V_{pk} is the maximum peak value. All the CSF values are scaled with the processed power, making the CSF a dimensionless quantity.

$$SCSF_i = \frac{\sum_j W_j}{W_i} \cdot \frac{V_{max}^2 \cdot I_{rms}^2}{P^2} \quad (15)$$

$$WCSF_i = \frac{\sum_j W_j}{W_i} \cdot \frac{V_{max}^2 \cdot I_{rms}^2}{P^2} \quad (16)$$

$$CCSF_i = \frac{\sum_j W_j}{W_i} \cdot \frac{V_{pk}^2 \cdot I_{rms}^2}{P^2} \quad (17)$$

$$SCSF = \sum_{Semiconductors} SCSF_i \quad (18)$$

$$WCSF = \sum_{Windings} WCSF_i \quad (19)$$

$$CCSF = \sum_{Capacitors} CCSF_i \quad (20)$$

In (15)-(17) $\sum_j W_j$ represent the total available resources for each component and W_i represent the amount of resources assigned to the specific component. In order to minimize the different CSF values, the resources can be differently

distributed by using the W_i weighting factors however, as first iteration the resources are supposed equally distributed. In (18)-(20) each component represents a specific stress; therefore, a CSF comparison requires to compare only components of the same type.

A. CSF Analysis for the Candidate Topologies

The results from the CSF analysis are presented in Fig. 5. The plots are shown as function of the converters operating voltages and currents on the low voltage side. The converter voltage on the high voltage side is fixed at 750 V and a summary of the dc-dc converter specifications used for the analysis is presented in Table II. The maximums values have been limited in order to have more comprehensive plots.

From Fig. 5 it can be observed that the values of the IFBBC's CSF (SCSF, WCSF and CCSF shown in Fig. 5a, 5b and 5c respectively) increase as the converter operating voltage on the LV-side decreases and the values are independent on the operating current. The transformer turns ratio also affects the values of the CSF; in the case of the IFBBC, the transformer turns ratio $n=8$ is chosen in order to minimize the voltage stress on the LV-side power semiconductors. Similarly, the SCSF, WCSF and CCSF values for the DAB topology are presented in Fig. 5d, 5e and 5f respectively. In this case, all the CSF values vary with both converter voltage and current. This is due to the variation of the phase-shift angle that controls the power flow in the DAB. For all the CSF values of the DAB, there is a minimum defined by the transformer voltage transfer ratio between primary and secondary. In this case, the optimal transformer ratio $n=14$ was selected in the middle of the

converter voltage operating range on the LV-side. By varying the number of transformer turns, it is possible to move the CSF minima to other converter operating points.

There is a major difference in the distribution of the CSF values within the two analyzed topologies: for specific operating points, the minimum value of SCSF and CCSF is lower for the DAB topology compared to the IFBBC. However, the IFBBC has lower WCSF over the entire converter operating range. In all the CSF values the DAB has a minimum at 54 V and 7 A; while, the IFBBC has always a minimum at 80 V on the converter LV-side. The major difference in the CSF values is observed for the WCSF: the IFBBC has very low WCSF while these values for the DAB are large due to the large ac-currents and voltages that continuously stress the magnetic components. The CSF values of the IFBBC increase at low voltage levels; this trend is homogeneous over the entire converter operating range. On the other hand, in the DAB, the CSF values widely increase especially at low current levels, when the converter is operating outside its optimal voltage transfer ratio, e.g. 54 V with $n=14$.

Fig. 5 IFBBC topology CSF: SCSF in (a), WCSF in (b) and CCSF in (c). DAB topology CSF: SCSF in (d), WCSF in (e) and CCSF in (f). Black dots in (d) and in (f) represent the intersection between the IFBBC CSF and the DAB CSF values.

TABLE III
AVERAGE CSF VALUES DEPENDING ON THE OPERATING MODE

Topology	SCSF	WCSF	CCSF
IFBBC FC-mode	217.8	6.5	11.3
DAB FC-mode	816.3	153.1	51.0
IFBBC EC-mode	109.0	5.5	5.3
DAB EC-mode	259.7	48.7	16.2

In order to select the most suitable topology, it is not only necessary to perform a CSF analysis, but it is also necessary to consider the overall system in which the dc-dc converter is expected to operate. Based on the cells stack characteristics presented in Section II, an I-V curve of the cell stack is built for both SOFC and SOEC operating modes [9]. The I-V model is used to determine all converter operating points. The converter CSF values are computed and averaged for all SOFC operating points and for all SOEC operating points. The obtained CSF values for SOFC and SOEC operation of the dc-dc converter are presented in Table III.

From the averaged CSF values it is interesting to observe that for both system operating modes (SOFC and SOEC) the average CSF of the IFBBC is significantly lower than the DAB. The IFBBC's average SCSF in FC-mode is 1/4 of the DAB's average SCSF in the same mode. The difference in average SCSF is to 1/2-1/3 in EC-mode. Similar differences are observed also in the average CCSF values. The IFBBC's average CCSF is about five times lower in FC-mode than the DAB; this difference is reduced down to three times for the EC operating mode. The largest difference is observed in the magnetic components stress factor, in this case the IFBBC has an average WCSF which is almost 1/20th in FC-mode and 1/10th in EC-mode compared to the DAB's average WCSF. The DAB has a high average WCSF due to the ac current circulating in the transformer windings and to the fixed duty cycle of the phase-shift modulation.

On overall, the IFBBC has lower CSF values and a more homogeneous variation over the entire converter operating range. It should be observed that for fixed operating points the DAB would be preferable. However, when computing

TABLE IV
IFBBC DC-DC CONVERTER CHARACTERISTICS

Voltage primary side (LV)	30-80 V
Maximum current primary side	80 A
Voltage secondary side (HV)	700-800 V
Maximum power	2400-6400 W
Low Voltage (LV) side MOSFETs	120V 4.1m Ω TK72E12N1 2 in parallel
High Voltage (HV) side SiC MOSFETs	SCT30N120 SiC MOS C4D15120 SiC diode
Inductor	20 μ H
Inductor core size/material	3xE6030 KoolMu 90-26-90
Turn ratio n	1:8
Transformer core size/material	2xE64 pairs/Magnetics R
Switching frequency	40 kHz

averaged values, the large CSF values that are observed in non-optimal operating points of the DAB give large contribution and therefore, they have a large weight on the overall average.

V. EXPERIMENTAL PROTOTYPE OF A ISOLATED FULL BRIDGE BOOST CONVERTER

The dc-dc converter for fuel cells is expected to operate with a variety of cells stacks; therefore the dc-dc converter is required to operate over the entire I-V specification in Table I. This requirement is necessary since the SOEC/SOFC technology if not fully mature and variations in the I-V characteristics are expected. Moreover, the I-V curve of SOEC/SOEC stacks depends also on the stack operating conditions (temperature, fuel composition, etc.). The IFBBC topology is selected since it has a more homogenous distribution of the CSF thus, a more homogeneous distribution of the converter efficiency is expected.

The converter prototype of a high efficiency IFBBC for bidirectional fuel cells, shown in Fig. 6a, has been developed and tested. The initial converter prototype was based on Si MOSFETs, Si IGBTs and SiC diodes. This prototype was capable of achieving efficiencies up to 97.8% and 96.5% [9][10] depending on the power flow direction. The converter was then updated with SiC MOSFETs in the full bridge of the converter HV-side. Its main operating waveforms at 60 V 80 A are shown in Fig. 6b and a summary of the converter

Fig. 6 IFBBC prototype highlighted its main components (a) and main operating waveforms at 60 V 80 A (b).

Fig. 7 Measured efficiency of the IFBBC prototype for fuel cell operating mode (a) and for electrolyzer cell operating mode (b). Peak efficiency in SOFC mode 98.2% and in SOEC mode 97.45%. Darkened area indicates current limitation of the dc-dc converter.

characteristics is presented in Table IV. The converter magnetics are based on custom planar cores in high frequency ferrite for the transformer (Magnetics R-type material) and in distributed gap material (Magnetics KoolMu) for the boost inductor.

The new converter based SiC active switches is capable of achieving efficiencies up to 98.2% when operating in fuel cell mode (power flow from the converter LV-side to the HV-side) and up to 97.45% with reversed power flow, as shown in Fig. 7a and 7b respectively. The highest dc-dc conversion efficiencies are always measured with a current of ~40 A and at the highest converter voltage on the LV-side (80 V). At the lowest voltage on the LV-size (30 V) the converter achieved an efficiency above 96% and 95% depending on the direction of the converter power flow, as shown in Fig. 7a and 7b.

VI. CONCLUSIONS

This paper presents a component stress factor (CSF) analysis of the isolated full bridge boost converter (IFBBC) and of the dual active bridge (DAB) operating with phase-shift modulation. The analysis focuses on bidirectional dc-dc converters for fuel cell applications since they require wide operating voltage and current ranges.

The analysis highlights that the DAB has a lower absolute CSF however, the CSF in the DAB rapidly increases as the operating conditions deviate from the optimal operating point. In the DAB, the CSF varies along with both converter voltage and current; while in the IFBBC it depends only on the converter operating voltage. The IFBBC has a lower at average CSF which is also independent from the converter current. The bidirectional fuel cell I-V characteristic has been used to compute an average CSF for both fuel cell and electrolyzer cell operating modes. This highlighted that the IFBBC is a preferable topology for wide operating voltage ranges.

A 6 kW (30-80 V 80 A boosted up to 700-800 V) converter prototype of an IFBBC has been developed. The converter is

based on high current fully planar magnetics and is capable of achieving peak efficiency of 98.2% in fuel cell mode and of 97.45% in regenerative mode.

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