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Fabrication of deep-profile Al-doped ZnO one- and two-dimensional lattices as plasmonic elements

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ABSTRACT

In this work, we report on fabrication of deep-profile one- and two-dimensional lattices made from Al-doped ZnO (AZO). AZO is considered as an alternative plasmonic material having the real part of the permittivity negative in the near infrared range. The exact position of the plasma frequency of AZO is doping concentration dependent, allowing for tuning possibilities. In addition, the thickness of the AZO film also affects its material properties. Physical vapor deposition techniques typically applied for AZO coating do not enable deep profiling of a plasmonic structure. Using the atomic layer deposition technique, a highly conformal deposition method, allows us to fabricate high-aspect ratio structures such as one-dimensional lattices with a period of 400 nm and size of the lamina of 200 nm in width and 3 µm in depth. Thus, our structures have an aspect ratio of 1:15 and are homogeneous on areas of 2x2 cm² and more. We also produce two-dimensional arrays of circular nanopillars with similar dimensions. Instead of nanopillars hollow tubes with a wall thickness on demand from 20 nm up to a complete fill can be fabricated.

Keywords: AZO; plasmonic material; high aspect ratio lattices; surface waves; atomic layer deposition

1. INTRODUCTION

Plasmonics has been an ever-growing subject of research in photonics during the past few decades. It has numerous applications ranging from subwavelength optical devices and spasers to chemical and bio-sensing. Traditionally, noble metals are considered as plasmonic materials in the visible and telecom ranges, with a plasma frequency being typically in the ultraviolet wavelength range. However, they are hardly applicable in the mid-IR, or even in near-IR, where their dielectric permittivity becomes too negative1. The quest for a suitable plasmonic material in the near- and mid-IR, which is of special interest for various applications including, but not limited to, sensing, focusing, and waveguiding1 has been open for a while.

Transparent conducting oxides (TCOs) have attracted significant attention as alternative plasmonic materials to conventional metals1-3. Many studies focus on aluminum- or gallium- doped zinc oxides4-7 or tin-doped indium oxide8,9 due to their low loss and metallic behavior in the near infrared. One more advantage of TCOs is the possibility of tuning their permittivity by design through deciding the dopants or the ratio of different components10, thus constituting an advantage over metals having fixed permittivity values. By exploring this tuning possibility, plasmonic modulators where the TCOs are the active parts were investigated theoretically11 and experimentally12.

Originally most of the TCOs were deposited by pulsed layer depositions (PLD). However, recently it has been shown that good control in AZO growth can be achieved using the atomic layer deposition (ALD) technique5, 13. In this work, we report on fabrication of deeply profiled one- and two-dimensional lattices made from AZO to be exploited as plasmonic structures in the near- and mid-IR ranges.
2. **FABRICATION OF HIGH ASPECT RATIO STRUCTURES**

Nanoscale realization of one- and two-dimensional (1D/2D) high aspect ratio structures with precisely controlled shape and profile depth is a huge challenge and put heavy demands on the fabrication techniques. In a top-down approach one would normally seek a plasma based reactive ion etching (RIE) technique in combination with a lithographically defined masking layer (soft or hard mask) to define the structures. For deep-profile structures it is essential to achieve an etch process with a proper selectivity towards the masking layer and at the same time keep a high degree of anisotropy during etching. This is a highly non-trivial task for most materials including TCOs.

To circumvent this obstacle, Huang et al. suggested as a general fabrication scheme to combine ALD layer formation of the functional material in a pre-defined template with a subsequent selective removal of the template leaving a negative replica of the template structure in the functional material\textsuperscript{14}. ALD is a self-limiting growth method governed by surface reaction of two sequentially precursor exposures forming coatings with monolayer precision. This approach is inherently conformal thus the obtained structures will be the “negative” of the template with a high fidelity towards the template structure. In this way the pattern formation can be defined e.g. by a Si template, using deep reactive ion etching (DRIE) of Si, which is a matured and well established technology for high aspect ratio structures\textsuperscript{15}. Still, finding a proper selective etch for the Si template removal remains an issue.

In the present work, we have adapted the fabrication scheme of Huang et al. to our material platforms. Figure 1 (a)-(d) illustrates the individual process steps. In the first step (a) a Si template is defined by DRIE in a carefully optimized process in order to obtain trenches with straight sidewalls and the required etch depth. Next, the trench structure is coated using ALD for conformal layer growth (b). This is followed by a top layer removal step whereby the Si template core between the ALD layers is exposed (c) and finally the Si core is removed by a selective Si etch (d).

Figure 1 (e)-(h) shows an example of how this scheme can be applied for realizing a 1D trench structure in TiO\textsubscript{2}. In this case the trench pattern for the Si template was defined by deep UV lithography (DUV stepper: Canon FPA-3000 EX4) followed by DRIE etching (DRIE-Pegasus from SPTS) in a switched (Bosch) process consisting of cyclic steps of etching and surface passivation. The processing temperature was kept at 0°C and the process pressure at 10 mTorr.

Following the template fabrication, conformal ALD layer growth was conducted using a hot wall thermal ALD system (Picosun R200). Here, the TiO\textsubscript{2} layer was deposited at 150°C using TiCl\textsubscript{4} and deionized water (DIW, H\textsubscript{2}O) as the precursors (both from Sigma-Aldrich). At higher temperatures, the crystalline anatase phase transition would take over and increase the surface roughness\textsuperscript{16}. Therefore, to keep the amorphous phase, the deposition was done at or below 150°C. The number of cycles (2500) was chosen to achieve the desired thickness of approx. 100 nm TiO\textsubscript{2} coating in
order to entirely fill the DRIE silicon template. The top TiO₂ layer was removed using a Cl₂ based inductively coupled plasma etching (Multiplex ICP Etcher from SPTS). It is important that the top layer etch is strictly monitored, since overetching will result in a prominent attack of the Si core below it. Then, the exposed Si core of the template was removed (etched) using a continuous ICP etching process based on SF₆. The chemistry for this Si etch was chosen to ensure high selectivity with respect to the TiO₂ ALD coating. As such, we observed no indications of TiO₂ degradation during this step. We would like to note that the Si etch is nonlinear in time as the etch rate is reduced with the etch depth. This fact is the reason why the TiO₂ removal in the previous step needs to be strictly controlled since an overetching will result in an unknown Si core height. Thereby the subsequent core removal by SF₆ ICP etching is prone to cause a collapse of the grating structures by underetching the TiO₂ grid as the bottom of the trench structure is reached. A slight overetch of silicon during the TiO₂ removal step can be seen in Figure 1g. In the shown example the TiO₂ grating consists of lamellas with a pitch of 400 nm, a thickness of 200 nm and a height of 3 µm thus having an aspect ratio of 1:15 of the fabricated trench structure. A detailed description of the applied process parameters can be found in17.

The fabrication scheme can be extended to multilayer structures as shown in the SEM cross section micrographs in figure 2. Here, the potential of the high conformity growth of the ALD layers is clearly illustrated e.g. for realizing superlattices (see insets (a) and (b) showing magnification of the top- and bottom part, respectively). In this case the Si template formation with a trench depth of 4 µm was followed by sequential growth of Al₂O₃ (3 bright layers) and TiO₂ (2 dark layers), respectively, each with a layer thickness of 30 nm. However, due to slightly negative tapered sidewalls in the Si template the second Al₂O₃ layer closed the top part of the trench (inset (a)) before the second TiO₂ deposition was initiated thus blocking for further deposition in the trench structure. This example emphasizes the importance of controlling the straightness of the sidewalls throughout the whole depth of the trench profile.

![Figure 2. Al₂O₃/TiO₂ multilayers deposited on a Si DRIE template grating (each ALD layer is approx. 30 nm thick). Dark layers are TiO₂, bright ones are Al₂O₃. Insets show (a) Top part magnification, and (b) Bottom part magnification.](image)

### 3. AZO: DEPOSITION SCHEME AND MATERIAL PROPERTIES

In the pursue of new plasmonic materials for replacing the noble metals, doped oxide semiconductors such as TCOs have attracted substantial attention in recent years. Among these AZO deposited by PLD has shown very promising results as a plasmonic component for a hyperbolic metamaterial in the near-IR region5. Following the fabrication scheme described in the previous section, we have applied and optimized ALD for the AZO deposition in the pre-structured Si template.

Prior to the deposition in the Si templates the ALD process was investigated on planar Si(100) substrates for determining the growth rates (not shown here), chemical composition, and film morphology. The details of this study will be
published elsewhere. In short, a thorough investigation of various dopant concentrations of the AZO was conducted. In this study, a hot wall thermal ALD system (Picosun R200) was used and the precursors were obtained from Strem Chemicals. ZnO ALD was deposited using diethylzinc (DEZ, Zn(C₂H₅)₂) and deionized water (DIW, H₂O) as precursors, whereas Al₂O₃ was deposited using trimethylaluminum (TMA, Al(CH₃)₃) and DIW. The Al-doping of the ZnO was made by introducing a single cycle of (TMA+DIW) into the ZnO matrix made by “n” (DEZ+DIW) cycles. “n” was varied between 5 and 35, so each AZO sample was nominated by the certain Al doping level: D05, D10, ..., D35. While the dopant concentration was varied, the total film thickness was kept constant at approx. 100 nm.

Selected results from the characterization of the grown AZO D15 films are shown in figure 3 which are examples of (a) an x-ray photoelectron spectroscopy (XPS) measurement revealing the chemical composition and (b) a SEM image (inset in (b) is an atomic force microscopy scan of the surface) showing the surface morphology. XPS chemical trace analysis (performed using Thermo Advantage v5.948 software from Thermo Scientific) revealed a high quality chemical film with no observable impurities. The Al concentration in D15 doped AZO film was found to be 12.8 At. %. Prior to the XPS measurements the film surface was sputtered in-situ with Ar+ ions in order to get rid of the native airborne hydrocarbons.

The surface morphology is found to be dependent on growth temperature and doping level. In general, the higher the Al doping level, the smoother the surface. However, a minimum of 4-6 (DEZ+DIW) cycles are required for stable nucleation of the ZnO layer on Al₂O₃ setting an upper limit for the Al doping concentration (around D05). For the AZO D15 films we find a relative smooth surface with a roughness of approx. 2.35 nm RMS. These findings are in agreement with previously reported results.

Regarding the optical properties of the AZO films spectroscopic ellipsometry data were collected using a VASE Ellipsometer from J.A. Woollam Co. In spectroscopic ellipsometry the parameters ψ and Δ are measured over a certain wavelength range (this case 210 – 1690 nm) where \( \psi \) and \( \Delta \) are defined by \( r_p/r_s = \tan(\psi)e^{-i\Delta} \), where \( r_p \) and \( r_s \) are the reflection coefficients of p and s polarized light. The experimentally obtained values of \( \psi \) and \( \Delta \) were fitted using Kramers-Kronig relations. Figure 4 shows an example of the optical permittivity measurements obtained as a function of wavelength in the range of 210 - 1690 nm. In figure 4 both the real- (\( \varepsilon_1 \)) and imaginary (\( \varepsilon_2 \)) part of the dielectric function are displayed. For comparison, the permittivity was measured both for AZO D15 and undoped ZnO films each having a film thickness of 100 nm.
Fitting these data with a Drude model we can estimate that the real part of permittivity of AZO D15 approaches zero at a wavelength of approx. 2.5 μm, in good agreement with recent studies\textsuperscript{20,21}.

4. FABRICATION OF AZO 1D- AND 2D-STRUCTURES

The next step was to apply the high aspect ratio fabrication scheme described above on the AZO material platform to form 1D- and 2D-structures. In the case of 1D grating structures, we found that the scheme described in section 2 (see figure 1(a)-(d)) works very well for the AZO with minor modifications. Step 1 and 2 (figure 1(a) and (b)) were the same except for the ALD layer material (now AZO). For the top layer removal a pure physical etching step was used by applying an Ar\textsuperscript{+} ion sputter etching (Ionfab 300 Plus from Oxford Instruments). Here, the process was tuned to an etch rate of 20 nm/min which provided a well-controlled top layer breakthrough. Following this, the exposed Si core could be removed selectively by an isotropic SF\textsubscript{6} based RIE process without any noticeable influence of the AZO layer quality (very high selectivity).

It should be noted that the structure height is determined by the initial trench height of the Si template and therefore in this case depends on the Si etch rate and time applied in step 1 (figure 1(a)). As the etch rate vary with etch depth or depends on the aspect ratio of the structure to be defined (aspect ratio dependent etching – ARDE\textsuperscript{22}) it can be a major advantage to control the structure height by a built-in etch stop. This can be achieved by using a silicon-on-insulator (SOI) wafer with a buried SiO\textsubscript{2} layer as starting material and a device layer thickness corresponding to the wanted structure height. This ensures full control of the trench height in the formation of the Si template as the DRIE step can be designed to stop at the buried SiO\textsubscript{2} layer.

In the present study for the fabrication of 2D structures (nanopillars, figure 5), home-made SOI wafers were manufactured by growing an amorphous Si layer using low pressure chemical vapor deposition (LPCVD) on top of an oxidized Si(100) wafer. First, standard Si(100) wafers were oxidized in a conventional quartz tube furnace (from Tempress) using a dry oxidation process based on O\textsubscript{2} at 1100°C, resulting in a 200 nm SiO\textsubscript{2} on Si. Next, a 1 μm amorphous Si layer was grown on the SiO\textsubscript{2} surface using a conventional LPCVD process (LPCVD furnace from...
Tempress) based on SiH₄ at 560°C. The surface roughness is below 1nm, thus of sufficient quality to be used as base material for the Si template. Figure 5 shows the final result of the fabricated 2-dimensional arrays of circular AZO D25 nanopillars. These pillars have a diameter of 250 nm, a height of 1 µm and a pitch of 500 nm.

![Figure 5. Example (SEM micrographs) of 2D deep-profile AZO array of circular nanopillars.](image)

5. CONCLUSION

To summarize, transparent conductive oxides offer new promising opportunities compared to the noble metals as plasmonic materials in the mid- and near-infrared due to the low loss metallic behavior in this region and the possibility of tuning the permittivity. However, to exploit the various applications in optical devices it is essential to develop techniques for structuring these materials at nanoscale. We present a new scheme for fabricating well-controlled deep-profile 1- and 2-D nanoscale structures in AZO and other oxides. The examples presented in the present work illustrate the huge potential of this technique as a highly versatile and generic fabrication scheme for a large variety of materials and structural elements. With the inherent accuracy and scalability of the lithography- and etching tools applied, it is possible to realize large coherent areas of nanostructural elements on a 150 mm wafer (step-and-repeat of printed field sizes of 22x26 mm² for the DUV stepper) with feature sizes down to ca. 200 nm. Thus, the full potential of mass production of nanostructural elements in new material platforms (and combinations of these) such as TCO materials for plasmonic structures is opened for advanced metamaterials fabrication.

REFERENCES

