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Hardware- and Software-in-the-loop Simulation for Parameterizing the Model and Control of Synchronous Condensers

Ha Thi Nguyen, Student Member, IEEE, Guangya Yang, Senior Member, IEEE, Arne Hejde Nielsen, Senior Member, IEEE, and Peter Høigaaed Jensen

Abstract—The rapid increase of converter-based generation in the grid causes significant changes in the system dynamic characteristics. These changes require verification and testing of the reliability, optimization, and functionality of power system elements and controllers, which become more important for guaranteeing a secure operation. This paper first introduces a hardware-in-the-loop (HiL) setup of an automatic voltage regulator (AVR) control system and then proposes a software-in-the-loop (SiL) setup for the parameterization of an IEEE standard AVR/excitation system model and a power oscillation damping (POD) controller of synchronous condenser. The AVR hardware is interfaced with a simulated grid in real-time digital simulator (RTDS) using EMT simulation to evaluate its control functions. Parameter optimization for the POD and the AVR simulation model by SiL simulation is implemented in a closed loop between RTDS and Matlab/Simulink interfaced through OPC to satisfy the optimization objectives. By tests executed on the future Western Danish power system, parameter optimization of the POD and IEEE standard AVR model, and the function of AVR hardware are successfully implemented and verified.

Index Terms—Automatic voltage regulator, hardware-in-the-loop, parameter optimization, power oscillation damping, software-in-the-loop.

I. INTRODUCTION

The major energy transition in the stepwise replacement of synchronous-machine-based systems by renewable-based systems renders the system operation more challenging due to the different physical characteristics and interaction with the grid of converter-interfaced components. Traditional power plants are usually immense synchronous generators that are capable of providing ancillary services for balancing and transient characteristics. Conversely, the majority of renewable energy generators are interfaced to the grid via power electronic converters that have different characteristics and are often controlled in such a way that they are independent of the system frequency. In addition, high importing HVDC interconnections are also expected to replace the traditional generation. These factors have introduced many challenges to secure operation and forced power systems to operate in low inertia conditions. The combination of reduced system inertia and intermittent renewable sources also causes considerable changes in the system dynamic characteristics. Oscillatory issues from low inertia operating conditions, poor frequency control support and weak interconnections among power systems are more significant. In [1], [2], different frequency range oscillations in power systems with high shares of renewable sources are found in Texas and China power systems. These oscillations are caused by converter-based generations connected to weak power systems [3].

From the above analysis, low inertia systems are inevitable in the green energy era, which introduces new stability issues and requirements for controls. This approach requires innovative solutions for verification and testing of the reliability, functionality and optimization of power system elements and controllers to guarantee a secure operation.

To address these issues, hardware-in-the-loop (HiL) tests for supplementary control have been introduced. In HiL simulation, the interface between the simulation system and the test component is being emulated such that the inputs to the component seem to be derived from a real system and the control signals from the component cause corresponding actions in the model. In [4], a real-time HiL test for an excitation control system of a synchronous generator using external stabilizing signals to improve oscillation damping is investigated. A control HiL for automatic voltage regulator is implemented in [5] to evaluate the control functions during steady-state and dynamic conditions. HiL simulation for superconducting DC power transmission system, which investigated the stability of a superconducting DC cable system in steady state and transient conditions, is presented in [6]. Reference [7] implements an HiL test for protective relaying control adjustments and training tools using the reduced Colombian power system. A HiL of distance protection relay of synchronous condensers under unbalanced faults is developed in [8], while a hardware simulation of a static VAR compensator for load compensation is designed and implemented in [9]. Furthermore, a real-time hardware and SiL simulation is developed to evaluate the functionality of decentralized distribution network control architecture [10]. A control HiL simulation of a permanent magnet synchronous motor in healthy and faulty conditions is proposed in [11]. The simulation proves that control HiL is a safe and relatively inexpensive environment for developing fault detection and diagnostic algorithms.

Parameter optimization of controllers is another approach to ensure the secure operation for modern power systems. Control engineers are concerned with the efficient implementation
of parameter optimization to maximize their testing. In the literature, several researchers have implemented optimization algorithms for determining an optimal parameter set of controllers using the Matlab program. Tuning parameter for a PID of an AVR control system [12], [13] or a PSS [14]–[17] for an SG in synchronous-machine-based systems is well researched using different algorithms. A new controller that interacts between the active power and reactive power channels of SCs to improve primary frequency control is investigated in [18]. This control method is able to assist the governor response by reactive power modulation to improve the frequency nadir and settling time. However, this method does not adequately deal with the oscillation damping and mention about the parameter optimization of the proposed controller. In [19], a genetic algorithm (GA) optimization is applied for tuning control parameters of the proportional integral controller subject to $H_{\infty}$ constraints in terms of linear matrix inequalities of FACTs controller. A PSS parameter optimization using a trajectory sensitivity approach for a multi-machine power system is examined in [21]. However, parameter optimization by the Matlab simulation using RMS simulation based on the system-theoretic approaches either linear model or other algorithms, which depends on the initial operating point with several simplifications, e.g., no consideration of discrete control behaviors like limiters, saturation. As a result, it is limited for evaluating the control system performance when it reaches the limits or discrete functions during disturbances. A gradient-based nonlinear parameter optimization of FACTs devices via Real-Time Digital Simulator (RTDS) is investigated in [22], [23]. However, the research is limited due to a lack of optimization algorithm implementation support in the RTDS.

The paper introduces a HiL setup for an automatic voltage regulator (AVR) and proposes software-in-the-loop (SiL) simulation for parameterization of a power oscillation damping (POD) controller incorporating synchronous condenser (SC) and an IEEE standard AVR model to evaluate the functionality and the optimization of these elements, as a part of the SCAPP project [24]. The system includes a simulated grid in RTDS interfaced with AVR hardware, which is employed in the field and differs from the IEEE model. Furthermore, a SiL among the RTDS, Matlab, and OPC using real-time EMT simulation for optimizing the parameter of the control and the model is proposed. The proposed method uses Prony analysis to extract the system information based on measurement data which is applicable in the field. On top of it, the proposed method tends to apply to converter-based systems where the mechanism and characteristics are completely different to the synchronous-machine-based systems, and the POD and AVR are focused on the SCs.

The paper aims to optimize parameters of POD control for damping oscillation and enhancing frequency stability and those of the IEEE standard AVR model to achieve a behavior that is similar to the AVR hardware and test the functionality of an AVR controller in hardware before commissioning the system for field demonstrations. From the test executed with the future Western Danish power system [25], the parameterization of the POD and the AVR model and the functionality of AVR features are successfully implemented and verified.

The remaining part of the paper is organized as follows. The system arrangement is generally introduced in Section II. Section III presents the AVR implemented in hardware which includes different limiters of SC. A SiL of the POD and PID parameter optimization is investigated in section IV. In section V, the different test results of the actual AVR system and parameterization based on SiL simulation are analyzed and verified. Some important conclusions are finally drawn in section VI.

II. SYSTEM ARRANGEMENT

The whole future Western Danish power system is modeled in RSCAD and run in real time at RTDS platform. This
system is interfaced with an AVR system in hardware through digital/analog inputs/outputs. In addition, a SiL simulation for parameterization is implemented with a communication of RTDS, Matlab, and OPC. The whole system arrangement is shown in Fig. 1.

The RTDS is a fully digital power system simulator operating in real time, which can solve the power system equations fast enough (typically 50 μs time step) to continuously produce output conditions that realistically represent conditions in the real network [26]. Therefore, it provides a solid framework for testing, development, and evaluating the performance of power system controllers under a similar condition to real environment in practice.

Fig. 2 shows the overall procedure of optimization process with SiL and HiL in sequences, where the entire optimization process is carried out in many successive runs until the parameter set \( x \) satisfies the user-defined stopping criteria.

![Fig. 2. The overall procedure of optimization process with SiL and HiL.](image)

III. AVR HARDWARE-IN-THE-LOOP SETUP

Fig. 3 shows a PQ capability diagram of the synchronous generator, which is considered to be a thermal diagram of the machine. The stator current limit represents the stator winding isotherm, the field current limit describes the rotor winding isotherm, and the limit in capacitive regime conditions acts for the isotherm of the frontal teeth. Regarding the synchronous condenser, the field current limit, the stator current limit, and the flux limit have a key role to guarantee operation in the steady-state stability margin. These limiters are embedded in the AVR hardware as the thermal protective functions, namely, underexcitation limiter (UEL), overexcitation limiter (OEL), and stator current limiter (SCL). The UEL prevents the synchronous condenser from operating below the end core limits and the steady-state stability limit; the OEL prevents the synchronous condenser from operating above the rotor winding heat limit; and the SCL protects the stator winding from operating over the heat limit. These limiters enable the SC to operate in the maximum transient to support the power system during disturbances while ensuring that SC operation remains within allowable regions, as indicated in Fig. 3.

![Fig. 3. The PQ capability diagram of the synchronous generator.](image)

The AVR model implemented in the HiL platform is based on PID control that is modified from the AVR model of the IEEE AC7B excitation system, as shown in Fig. 4 [27]. The AVR detects the terminal voltage of SC through voltage compensator and transforms the signal to \( V_C \), and then compares with the reference value \( V_{ref} \). The difference \( e \) between these two values works as the input for PID control to create the AVR output \( V_R \):

\[
V_R = K_P e + K_I \int_0^t e dt + K_D \frac{de}{dt} \tag{1}
\]

where \( K_P \), \( K_I \), and \( K_D \) are the proportion, integral, and differential gains of the PID control, respectively.

Whenever a difference between \( V_C \) and \( V_{ref} \) is detected, PID control immediately reacts to correct the deviation. The higher is \( K_P \), the faster is the correction. However, a large \( K_P \) can cause a large oscillation in the output. The static error of the terminal voltage is eliminated by the integral function, the lower is \( K_I \), the smaller is the overshoot obtained by the output; however, the system may experience more oscillations during the dynamic period. The differential function improves the disturbance rejection ability but may carry the noise of the input to the output. A low-pass filter \( \frac{1}{1+s\tau} \) is usually added for the derivative term to reduce the noise.

The value of \( V_R \) is converted to a percentage as the AVR set point sending to the frequency converter of the excitation system as shown in Fig. 4. The frequency converter, exciter, and diode bridge parts are implemented in RTDS with SC and the grid models. An AVR simulation based on the IEEE AC7B model with the parameter set of PID controller is optimized by an GA objective to build an AVR simulation having a similar behaviour to the AVR hardware.

Fig. 5 presents the configuration diagram of the system interfaced with the hardware-in-the-loop test of the AVR. The entire grid and SC model without the AVR system are simulated in RTDS with the dashed blue boundary, while the AVR system is implemented in the hardware with the dashed red boundary. The RTDS sends the three-phase current and voltage of the generator side, the excitation current and voltage, and the busbar frequency and voltage of the grid
side to the hardware platform, whereas the hardware platform transfers the AVR setpoint (AVR SP) and ON GRID signal to the RTDS [5] as shown in Fig. 5(a).

The entire SC system, including the AVR controller, is programmed in VisSim software in PC1 for the startup procedure of SC, whereas the AVR hardware is programmed in the programmable logic controller (PLC) S7-400 via WinCC, which is located at PC2, as shown in Fig. 5(a). The main components of the hardware platform consist of two PLCs S7-1500 and S7-400H and measurement transformers. S7-1500 is responsible for the measurement data and computation of signals from transformers, while S7-400H is in charge of AVR control functions. Three-phase 100 VAC voltage and 1 AAC current outputs from the OMICRON power amplifier are sent directly to the measurement transformers. These voltage and current signals transfer to S7-1500; after storing and processing these signals, they are sent to S7-400H, where the AVR is programmed. Regarding RTDS interface, RTDS can receive and send their analog and digital signals to HiL through Giga Transceiver I/O cards (GTIO) that include Giga Transceiver Analog Input/Output cards (GTAI/GTAO) and Giga Transceiver Digital Input/Output cards (GTDI/GTDO). With terminal voltage and current outputs of SC, the analog input of the external device requires other ranges which are 1 A for the current and 100 V for the voltage corresponding to their nominal values. A power amplifier is therefore used to satisfy the HiL required values as shown in Fig. 5(b). Meanwhile, the excitation current and voltage, as well as the frequency and voltage of busbar through scaling ratios inside of GTAO card are sent directly to the hardware. The ON GRID signal is sent to RTDS from the hardware through a GTDI card and AVR setpoint from the hardware is sent to RTDS via a GTAI card as shown in Fig. 5(b).

After executing the SC start sequences, the SC is synchronized with the system. Over-excitation, under-excitation conditions, and a load increase disturbance are implemented to verify the performance of AVR hardware and simulation with parameter optimization.

A. Underexcitation limiter implementation

When the SC becomes underexcited to the point that the negative reactive power may fall below the SC capability limit, the UEL will react by producing a positive error signal that will be supplied to the AVR to require the AVR to increase the output voltage by increasing the voltage setpoint until the SC operating point increases above the UEL limit line characteristics [28], [29]. One important function of the UEL is to prevent SCs from operating beyond their steady-state stability limits, as shown in Fig. 3. The underexcited operation is limited to a point, where a small minimum excitation level is required for stability reasons.

Fig. 6 shows the UEL limiter that is implemented in the PLC. The function is based on the measured active power, reactive power, and terminal voltage of the SC. The block \( Q(P) \) symbolizes a look-up table that converts P values to Q values. Whenever the measured reactive power Q is greater
than the output of \( Q(P) \), a difference is sent to a PI controller. Additionally, the output of the PI controller can be set to zero using the enable signal.

2. The frequency drops to an impermissible value at a constant generator voltage.

3. The frequency drops and the terminal voltage rises to an impermissible value.

The input of the VHL limiter is the ratio of the per unit machine terminal voltage to the per unit speed. When the ratio exceeds a maximum threshold \((V/f)_{SP}\), shown here as 1.1 pu, the difference is obtained by an interpolation function and is then integrated. The higher is the \((V/f)\) value, the faster the integrator will work. A negative signal \( V_{s}\) is sent to the AVR summing point. The negative signal reduces the voltage setpoint, and therefore, the SC output voltage decreases until the voltage to frequency ratio falls below the threshold.

When the difference becomes negative via the inequality block, the integrator is reset to zero to obtain null error signal becomes null, as shown in Fig. 8. Therefore, a \( V/f \) will change the SC output voltage but will not have an effect on the machine frequency [33].

**B. Overexcitation limiter implementation**

The system voltage reduction due to the switching operation or faults produces high reactive power requirements, by increasing the field current to maintain a constant generator voltage. In a remarkable system, the voltage drops, which may cause thermal overloading of the generator rotor winding if the generator voltage setpoint is not reduced within a certain time. In this situation, the OEL is automatically activated to limit the field current.

The purpose of the OEL is to limit the field current value to prevent the generator operating point from exceeding the field current limit and the rotor winding heating limit of Fig. 3 [30]–[32]. When the field current \( I_f \) exceeds the pickup value \( I_{fN} \), which is the nominal value of the field current, a positive signal will be supplied to the integrator, which enables the field current to attain a value of \( 1.5I_{fN} \) for 5 s. If the grid does not remove the disturbance after 5 s, the OEL will decrease the field current to \( 1.05I_{fN} \) as the permanent value, as shown in Fig. 7.

When the field current \( I_f \) reduces below the pickup value, a negative signal will reset the integrator, which will provide a null error signal to the AVR summing point. The overexcited capability is limited by the rotor maximum excitation current (thermal limit). The response of OEL differs from that of the ceiling current limiter by the response value and response time.

**C. VHL limiter implementation**

The VHL limiter is activated to prevent the overflux in the machine, which occurs when the permissible ratio of the voltage to the frequency is exceeded by the following cases:

1. The terminal voltage of the generator rises to an impermissible value at a constant frequency.

2. The frequency drops to an impermissible value at a constant generator voltage.

3. The frequency drops and the terminal voltage rises to an impermissible value.
vicinity of a power factor of one, a further change of the terminal voltage will not decrease the stator current and is blocked by the parameter $I_{R_{min}}$. The limiter is active when the stator current is higher than the pickup value $I_{R_{max}}$. If the difference is positive for more than 10 s, the limiter is activated and worked, as shown in Fig. 9. This output is subtracted to the AVR summing point. When the $V_{SCL}$ is equal to 0 for more than 2 s, the limiter is deactivated.

IV. SOFTWARE-IN-THE-LOOP SETUP

The prospective future Western Danish power system run in the RTDS platform is driven by a MatLab script for system startup and disturbance simulations. The data of the system are collected by an OPC server and sent directly to the MatLab workspace. In MatLab, the signal is processed and evaluated by the GA objective. These steps are iterative by a closed loop with RTDS, OPC, and MatLab communications, as shown in Figs. 1 and 2. The loop will continue until the objective function is satisfied to determine the optimal parameters. This SiL simulation for parameter optimization is applied separately for POD and PID controller of the AVR system where GA objective functions are completely different.

A. RTDS setup

The whole DK1 system is modeled and compiled in RSCAD/Draft, and then run and monitored in RSCAD/Runtime. To run the system by a MatLab script, a communication connection is established between RSCAD/Runtime and MatLab. Once the connection is implemented, RSCAD/Runtime becomes a socket server and MatLab becomes a socket client. RSCAD/Runtime is commanded and run in real-time by a MatLab script via the TCP server [26]. In addition, the new parameter set is updated via this communication.

B. OPC setup

To collect the data from RTDS, a communication channel between RTDS with MatrikonOPC is setup with 3 steps, including an RTDS network channel, an RTDS network host, and an RTDS DNP3, which are described in [34]. First, the RTDS network channel is setup where the protocol is defined. Second, the RTDS network host is established inside the network channel. In the network host, the IP address of the GTNET card of the RTDS and the port number that have been defined in the GTNET card of the RTDS platform are filled. Last, the RTDS DNP3 is created to view the signals that are collected from the RTDS. After the communication is established, the RTDS and OPC can exchange data with each other. The measurement data are collected in the OPC server and sent to MatLab for the next steps.

C. Matlab program

A MatLab script commands and runs the system in real-time in RSCAD/Runtime via TCP communication. To acquire the data at the OPC server, a connection is setup in a MatLab script to create a group object with defined properties. After obtaining the data and performing signal processing to convert to the proper format, the data are retrieved in the MatLab workplace for further analysis. When the data are sent to MatLab, OPC cleans up and disconnects to prepare for subsequent data collections.

With POD controller, frequency measurement is collected by OPC and sent to MatLab. Then a polynomial fitting is implemented to remove the fundamental frequency component. The oscillation component is analyzed by Prony analysis for extracting the frequency and damping ratio of the dominant oscillation mode. The damping ratio is maximized by GA objective function to determine the better parameters of POD and update the RTDS model for further verification.

The objective function of GA-based POD is the damping ratio maximization of the system oscillation mode, i.e.,

$$ f(x) = max \left\{ \xi = -\frac{\alpha}{\sqrt{\alpha^2 + \beta^2}} \right\} $$

where $\alpha$ and $\beta$ are real and imaginary parts of the dominant mode, respectively.

Meanwhile, with PID control of AVR, the objective function is the minimization of the difference of two AVR hardware and simulation outputs as follows:

$$ f(x) = min \left( \sum (E_{f_{ds}} - E_{f_{db}})^2 \right) $$

where $E_{f_{ds}}$ and $E_{f_{db}}$ are the outputs of the excitation with AVR simulation and that of hardware, respectively.

The parameter set is send to RSCAD/Runtime by a MatLab command for next iteration. These steps are continuously repeated until one of the termination parameters is achieved. The GA may be terminated after a certain number of generations when the objective value does not enhance after a certain generation. As a result, a near-optimal or optimal solution for parameter set is determined. The entire procedure is illustrated in Fig. 2.

V. CASE STUDY

To verify the parameter optimization for the PID controller of the AVR simulation model based on the GA, different tests, such as under-excitation and overexcitation tests and a load increase scenario, are investigated to compare the responses of the AVR hardware and simulation. In addition, a load increase scenario is investigated to evaluate the parameterization based on GA of the POD controller.

A. AVR hardware test

1) Underexcitation and overexcitation condition test: The voltage of the grid side suddenly increases 0.1 pu at $t = 0.75$ s, SC immediately absorbs reactive power from the grid side to lower the voltage. The comparative results between AVR controller of HiL and that of simulation are shown in Fig. 10 with dash red curves for hardware and solid black ones for simulation.

As can be seen in Fig. 10(b), a natural response of SC rapidly absorbs reactive power to against the voltage increase
during transient period, AVR control following response to absorb more reactive power which lead to the voltage starts going down and gets back the pre-disturbance point. It should be mentioned that AVR hardware control responds a bit slower than that of simulation due to the signal transferring time, which causes the terminal voltage of SC keep increasing a bit further 1.03 pu instead of 1.028 pu with that of simulation before getting the same new steady-state operating point after the disturbance. A similar pattern can be observed in Fig. 11 with overexcitation condition when the grid voltage decrease 0.1 pu. It is noteworthy that the active power responses of AVR hardware and simulation are almost similar because the AVR controller seems insensitive to the active power. 

2) Load increase: Another test to verify the response of AVR hardware and simulation is investigated during 150 MW load increase at t = 0.75 s, as shown in Fig. 12. From the comparative results with simulation and hardware of AVR, the reactive power responses are similar for both cases. They quickly increase to counteract the voltage decrease and bring the terminal voltage back to the pre-disturbance point, increasing from around 4 Mvar to 61 Mvar and settling down at approximately 38 Mvar after around 10 s. As a result, the terminal voltage response is similar for two AVR cases. The active power responses are almost similar in terms of magnitude and settling time for two approaches. It can be concluded that parameter optimization based on SiL offers a parameter set for AVR simulation to have a similar behaviour to AVR hardware.

B. POD parameterization test

In order to verify the performance of SiL simulation for POD parameter optimization, a load increase disturbance is investigated in this section. Fig. 13 shows comparative results of system frequency, ROCOF, active power on transmission
As expected, the active power of SC rapidly supports kinetic energy and quickly settles down with POD controller as seen in Fig. 13(d). As a result, the power oscillation and frequency stability are significantly improved during the disturbance with POD controller.

The parameters shown in Table I summarize the Prony analysis of the dominant mode with a significant enhancement of the system with POD controller based on nonlinear optimization algorithms. It can be concluded that with parameterization, the POD controller performs a significant enhancement in damping oscillation and frequency stability.

It is worth to mention that the system response is similar for AVR hardware and that of simulation. It can be concluded that parameter optimization based on SiL satisfies the optimization objective.

VI. CONCLUSION

The paper firstly presents a HiL test for the AVR of SC before the deployment in the field. Different tests such as underexcitation and overexcitation conditions are implemented for AVR hardware which demonstrates the possible applications of HiL to validate and adjust the control function of the equipment in steady state and dynamics conditions before final construction implementation with lower cost, higher efficiency, and more flexibility.

Furthermore, a SiL simulation using EMT simulation for parameter optimization based on a nonlinear optimization tool of POD and PID controllers is proposed to offer a near-optimal or optimal solution for the control parameter set which can help control designers saving a lot of time. GA is selected here since it is a typical approach for tackling complex optimization problems. This SiL simulation can also solve different issues in power systems, where the RTDS is replaced by the real system, OPC represents the data acquisition system, and Matlab acts as the control center.
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