A Controller Architecture With Anti-Windup

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A controller architecture with anti-windup

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Abstract—This paper presents a framework for anti-windup controllers based on the Youla-Jabr-Bongiorno-Kucera (YJBK) parameterization. Applying this architecture gives an additional YJBK matrix transfer function related to the input saturation. This additional YJBK transfer function can be applied for optimizing the feedback loop around the input saturation. Further, the connection with other anti-windup controller architectures is also considered in this paper.

Index Terms—Control system architecture, linear systems

I. INTRODUCTION

SATURATION in control systems is a known problem. Almost every system includes input saturation due to the limitation in the actuators. It is therefore relevant to consider a system with input saturation. This has been investigated in a large number of papers and books in connection with traditional control, as well as in connection with optimal and robust control. Throughout the years, a number of results dealing with analysis and design of anti-windup controllers for actuator saturates have been published. These methods include different design methods [1], [3], [4], [5], [8], [13], [18], [19], different controller architectures, as well as the robustness aspect in anti-windup controller design, [2], [7], [17], to mention some of the central papers in the area. Some open problems are discussed in the tutorial papers [15], [20].

The main focus in this paper is to apply the YJBK controller architecture in connection with input saturation. The YJBK architecture has previously been applied in connection with saturation, see e.g. [14] and [7]. In [14], the YJBK parameterization is included in an existing anti-windup controller, and applied for the optimization of the anti-windup part. Equivalent in [7], the YJBK parameterization is included in an existing anti-windup controller with respect to robustness in the closed-loop system.

In this paper, the YJBK architecture is applied directly in connection with input saturation. This can be done by using some known results from the YJBK parameterization, [9], [10], [16]. Together with the implementation of the YJBK controller described in [11], a YJBK architecture including an anti-windup part, gets a simple structure.

The relation with other anti-windup architectures is also considered. The anti-windup architecture by Weston and Postlethwaite (named as the W-P architecture in the following) described in e.g. [4], [7], [13], [19] and the architecture by Kothare et.al. described in [6] is considered. It is shown that the W-P architecture and the YJBK architecture are equivalent. A W-P controller can be implemented in the YJBK architecture as well as the other way around. Further, it is shown that the architecture by Kothare et.al is as a special case of the W-P architecture and the YJBK architecture.

The rest of this paper is organized as follows. The system set-up and some preliminary results are given in Section II. The saturation setup described in the YJBK architecture is given in Section III. The relation between different anti-windup controllers is described in Section IV. A closed-loop stability is analyzed in Section V. An example is given in Section VI, and the paper is closed with a conclusion in Section VII.
where the two Bezout matrices are given by
\[ Z^{-1} = \begin{pmatrix} M & U \\
N & V \end{pmatrix}, \quad \tilde{Z}^{-1} = \begin{pmatrix} \tilde{V} & -\tilde{U} \\
-\tilde{N} & M \end{pmatrix} \]

Based on the above coprime factorization, a parameterization of all controllers that stabilize the system in terms of a stable YJBK matrix transfer function \( Q \). Using a right factored form, all stabilizing controllers are given by [16]:
\[ K(Q) = (U + MQ)(V + NQ)^{-1}, \quad Q \in \mathcal{RH}_\infty \]  
(8)

or by using a left factored form:
\[ K(Q) = (\tilde{V} + Q\tilde{N})^{-1}(\tilde{U} + Q\tilde{M}), \quad Q \in \mathcal{RH}_\infty \]  
(9)

Using the Bezout equation, the controller given either by (8) or by (9) can be realized as a linear fractional transformation in the parameter \( Q \):
\[ K(Q) = \mathcal{F}_l \left( \begin{pmatrix} UV^{-1} & \tilde{V}^{-1} \\
V^{-1} & -V^{-1}N \end{pmatrix}, Q \right) = \mathcal{F}_l(J_K, Q) \]  
(10)

The YJBK parameterization for the left factored form in (9) is shown in Fig. 1, where \( \tilde{Z} \) and \( Q \) are given by:
\[ \tilde{Z} = \tilde{Z} - I_u, \quad Q = Q, \quad I_u = \text{diag}(I, 0) \]  
(11)

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Further, the two matrix transfer functions \( \tilde{Z} \) and \( Q \) in (11) are given by:
\[ \tilde{Z} = \tilde{Z}_{ext} - I_u, \quad Q = \begin{pmatrix} Q_s \end{pmatrix} \]  
(15)

where \( Q_s \) is related to the extra measurements included in the system.

It is also possible to derive a parameterization of all systems that are stabilized by one controller in terms of a stable matrix transfer function \( S \), i.e. the dual YJBK parameterization. The dual YJBK parameterization is given by [9], [16]:
\[ G_{yu}(S) = (\tilde{M} + S\tilde{U})^{-1}(\tilde{N} + S\tilde{V}), \quad S \in \mathcal{RH}_\infty \]  
(16)

using the left form. There exists also a right form for the dual YJBK parameterization. Further, \( S \) is given as an upper LFT by, [16]:
\[ S = \mathcal{F}_u(J_K, G_{yu}(S)) \]  
(17)

The matrix transfer function \( S \) is a function of the uncertainties in the system described with \( \Delta \) as described in (4). \( S(\Delta) \) is given by, [9]:
\[ S(\Delta) = \tilde{M}G_{yu}\Delta(I - (G_{zw} + G_{zu}U\tilde{M}G_{yw})\Delta)^{-1}G_{zu}M \]  
(18)

Note that the dual YJBK matrix transfer function \( S \) can be applied in different connections. From [9], [16], the closed-loop uncertain system is stable if the nominal closed-loop system is stable and the resulting \( S \) is stable. Here in the following, \( S \) will be used in connection with analysis of closed-loop stability when the YJBK controllers includes an anti-windup part.

III. SATURATION SET-UP

Consider now the system in (1) with a saturated control input vector given by \( u_s \). The saturated input vector \( u_s \) is given by
\[ u_s = \text{sat}(u) = [\text{sat}(u_1), \ldots, \text{sat}(u_m)]^T \]  
(19)

where \( \text{sat}(u_i) = \text{sign}(u_i) \min \{|u_i|, u_{\max,i}\}, \quad u_{\max,i} > 0 \quad \forall i \in \{i_1, \ldots, m\} \). Further, let \( U \) be given by:
\[ U := U_1 \times \cdots \times U_m \]  
(20)

where \( U_i := [-u_{\max,i}, u_{\max,i}] \). It is assumed that \( u_s \) can be estimated or measured. To simplify the equations, it is assumed in the following, that the inputs has been scaled such that all inputs has the same saturation, i.e. \( u_{\max,i} = u_{\max} \).

The saturation problem can be described by the general system setup given by (4). Let the input saturation be described by
\[ u_s = \text{sat}(u) = (1 + \psi_{\text{sat}}(u))u \]
\[ \psi_{\text{sat}}(u) = \text{diag}(\psi_1(u_1), \ldots, \psi_m(u_m)) \]  
(21)

where \( \psi_i(u_i) \in \mathbb{R} \) is a non-linear function given by:
\[ \psi_i(u_i) = \begin{cases} 0 & \text{for } u_i \in U_i \\ \frac{\text{sat}(u_i)}{|u_i|} - 1 & \text{for } u_i \notin U_i \end{cases} \]  
(22)
Using this description of the saturation, it can be formulated in the general system set-up in (1). This is obtained by describing $u_s$ as:

$$u_s = u + w_s, \quad w_s = \psi_s \zeta_s, \quad z_s = u$$  \hfill (23)

where $\zeta_s$ is the input vector to $\psi_s$ and output $w_s$ is the output vector from $\psi_s$. The general system set-up in (13) with the above extensions is now given by:

$$\Sigma_S : \begin{bmatrix} z_s \\ y_s \end{bmatrix} = \begin{bmatrix} 0 & I \\ G_{yu} & G_{yu} \end{bmatrix} \begin{bmatrix} w_s \\ u \end{bmatrix}$$ \hfill (24)

The feedback controller given by (5) is now given by:

$$\Sigma_{C,S} : \begin{cases} u = (K \theta) \begin{bmatrix} y \\ y_s \end{bmatrix} = K_S \begin{bmatrix} y \\ y_s \end{bmatrix} \end{cases}$$ \hfill (25)

A coprime factorization of the above $\Sigma_S$ and $K_S$ are given by:

$$\begin{pmatrix} G_{yu} \\ I \end{pmatrix} = \begin{pmatrix} N \\ M \end{pmatrix} M^{-1} = \begin{pmatrix} \tilde{M} & 0 \\ 0 & I \end{pmatrix}^{-1} \begin{pmatrix} \tilde{N} \\ I \end{pmatrix}$$

$$K_S = \begin{pmatrix} U & 0 \\ 0 & I \end{pmatrix}^{-1} \tilde{V}^{-1} \begin{pmatrix} \tilde{U} & 0 \end{pmatrix}$$ \hfill (26)

by using (14).

With the coprime factorization given above, it is simple to give a complete description of the YJBK parameterized controller using the compact description given in Fig. 1. In this case, $\tilde{Z}$, $Q$ and $\varepsilon_Q$ are given by:

$$\tilde{Z} = \begin{pmatrix} \tilde{V} - I & -\tilde{U} \\ -\tilde{N} & \tilde{M} \end{pmatrix}$$

$$Q = \begin{pmatrix} Q & Q_s \end{pmatrix}$$

$$\begin{pmatrix} -u \\ \varepsilon_Q \end{pmatrix} = \begin{pmatrix} u \\ \varepsilon_s \end{pmatrix}$$ \hfill (27)

where $\varepsilon_s$ is the saturation error vector, i.e. $\varepsilon_s = u_s - u$ and $Q_s$ is the YJBK matrix transfer function for feedback control from the saturation error vector.

The formulation of input saturation as a special case of system extension gives a controller that can be implemented directly in the YJBK controller architecture shown in Fig. 1. Further, the anti-windup part of the controller is related only to the YJBK matrix transfer function $Q_s$ and separated from $Q$.

In the following, only the part of the parameterization related to the input saturation in the system is considered, i.e. $Q = 0$. The case with $Q \neq 0$ is shortly discussed in Sec. V.

### IV. Relation to Other Anti-Windup Architectures

The YJBK architecture including an anti-windup part is related to other anti-windup architectures. Here we will consider the architecture by Weston and Postlethwaite described in e.g. [4], [7], [13], [19] and the architecture by Kothare et al. described in [6].

First, let’s consider the W-P architecture. The setup is shown in Fig. 2. where $\Theta_1$ and $\Theta_2$ are the design parameters.

![Fig. 2. The Weston-Postlethwaite controller architecture for systems with input saturation.](image)

Note that the residual vector $\varepsilon_s$ given in (27), and in the W-P controller architecture shown in Fig. 2 is the same vector. The control vector $u$ from the YJBK architecture in Fig. 1 with $\tilde{Z}$ in (27) is given by:

$$u = Ky + \tilde{V}^{-1} Q_s \varepsilon_s$$ \hfill (28)

The control input $u$ for the W-P architecture shown in Fig. 2 is given by:

$$u = Ky - K \Theta_2 \varepsilon_s + \Theta_1 \varepsilon_s$$ \hfill (29)

Let (29) be rewritten into:

$$u = Ky - \tilde{V}^{-1} (\tilde{U} \Theta_2 - \tilde{V} \Theta_1) \varepsilon_s$$ \hfill (30)

It is now easy to see that the W-P anti-windup controller architecture described by (29) can be implemented in the YJBK architecture. Selecting $Q_s$ as:

$$Q_s = -\tilde{U} \Theta_2 + \tilde{V} \Theta_1 = \begin{pmatrix} \tilde{V} & -\tilde{U} \end{pmatrix} \begin{pmatrix} \Theta_1 \\ \Theta_2 \end{pmatrix}$$ \hfill (31)

shows directly that two control vectors in (28) and (30) are identical. This shows that the W-P anti-windup controller can easily be implemented in the YJBK architecture. From [19], we have the following design of $\Theta_1$ and $\Theta_2$ are given by:

$$\begin{pmatrix} \Theta_1 \\ \Theta_2 \end{pmatrix} = \begin{pmatrix} M - I \\ G_{yu} M \end{pmatrix} = \begin{pmatrix} M - I \\ N \end{pmatrix}$$ \hfill (32)

Based on these matrices, $Q_s$ take then the following form:

$$Q_s = \begin{pmatrix} \tilde{V} & -\tilde{U} \end{pmatrix} \begin{pmatrix} M - I \\ N \end{pmatrix}$$ \hfill (33)

$$= I - \tilde{V} M$$

One of the important issues in the W-P architecture, compared with the YJBK architecture, is the implementation of the anti-windup part. An advantage in the W-P implementation, compared with the YJBK implementation, is that the additional control vectors from the anti-windup part are included in the loop before and after the controller. In the YJBK implementation, a single control input vector for the anti-windup part is injected into the nominal controller. This will in some cases not be acceptable because the nominal controller is modified.

It is therefore also relevant to consider the other way around. The starting point for calculating $\Theta_1$ and $\Theta_2$ based on $Q_s$...
from the YJBK implementation is to consider (28). Using the Bezout equation in (7), \( \hat{V}^{-1} \) can be
\[
\hat{V}^{-1} = M - \hat{V}^{-1} \hat{U} N = M - KN
\] (34)
Using (34) in (28) gives directly:
\[
u = Ky + (M - KN)Q_s \varepsilon_s
= Ky + MQ_s \varepsilon_s - KNQ_s \varepsilon_s
\] (35)
Compare (35) with (29) gives directly that \( \Theta_1 \) and \( \Theta_2 \) are given by:
\[
\begin{pmatrix} \Theta_1 \\ \Theta_2 \end{pmatrix} = \begin{pmatrix} M \\ N \end{pmatrix} Q_s + \begin{pmatrix} \Theta_{10} \\ \Theta_{20} \end{pmatrix}
\] (36)
where \( \Theta_{10} \) and \( \Theta_{20} \) are free parameters that need to satisfy:
\[
( \hat{V} - \hat{U} ) \begin{pmatrix} \Theta_{10} \\ \Theta_{20} \end{pmatrix} = 0
\]
Note that \( \Theta_1 \) and \( \Theta_2 \) are not unique when the calculation is based on \( Q_s \) from the YJBK parameterization.

Note that the reformulation of the control input vector from the YJBK controller is not only restricted to the anti-windup case, but can also be used in the YJBK architecture.

The W-P anti-windup controller given by \( Q_s \) in (33) has an interesting implementation in the general YJBK architecture. To see this, first, let the control vector in (28) be rewritten into:
\[
\hat{V} u = \hat{U} y + Q_s \varepsilon_s
\]
which gives
\[
u = \hat{U} y - (\hat{V} - I) u + Q_s \varepsilon_s
\] (37)
(33) together with (37) gives now:
\[
u = \hat{U} y - (\hat{V} - I) u + (I - \hat{V}) \varepsilon_s
= \hat{U} y - (\hat{V} - I)(u + \varepsilon_s)
= \hat{U} y - (\hat{V} - I) u_s
\] (38)
by using \( \varepsilon_s = u_s - u \).

Based on this control vector, the controller architecture from Fig. 1 takes the form as shown in Fig. 3.

The controller architecture showed in Fig. 3 is the same anti-windup architecture shown in [6]. The implementation showed in Fig. 3 demonstrates a simple way to include anti-windup in an existing controller. However, the controller still needs to be factorized and implemented using the coprime factors, so there is an internal feedback of the control vector. It is then simple to include the anti-windup part by moving the control feedback loop from the input side of the saturation block to the output side of the saturation block. The design freedom in this controller architecture is the coprime factorization of the controller. However, some controllers are implemented directly with this structure. An example of a controller with this structure is an observer based controller. The matrix transfer functions for the controller implementation shown in Fig. 3 are given by, [16]:
\[
( -(\hat{V} - I) \hat{U} ) = \begin{pmatrix} A + LC_y & F \\ -B_u + LD_y & -C_y \end{pmatrix}
\] (39)
where \( F \) and \( L \) are the state feedback gain and observer gain, respectively. An implementation of the full-order observer based feedback controller including an anti-windup part is shown in Fig. 4.

Fig 4 shows that the anti-windup controller from [6], or the W-P anti-windup controller given by the parameters in (32), can be implemented directly in an observer-based controller without including extra terms.

Using an observer-based feedback controller, it is possible to give a coprime factorization of system and controller as used in (39). A state space description for coprime factorization of general feedback controllers can be found in e.g. [16].

V. CLOSED-LOOP ANALYSIS

The stability analysis is based on the dual YJBK matrix transfer function \( S(\Delta) \) given in (18). The closed-loop system is stable if the nominal closed-loop system is stable, and the dual YJBK matrix transfer function \( S \) is stable, [9], [16]. Using \( \Delta = \psi_{sat} \) where \( \psi_{sat} \) is the non-linear function in (22) describing the input saturation, \( S(\psi_{sat}) \) can be then calculated by using the system \( \Sigma_S \) in (24), and the associated coprime matrices for the system in (26). \( S(\psi_{sat}) \) is given by:
\[
S(\psi_{sat}) = \begin{pmatrix} \hat{N} \\ I \end{pmatrix} \psi_{sat} (I - U \hat{N} \psi_{sat})^{-1} M
\] (40)
The influence from Q on the stability of the saturation loop has then been removed completely. A redesign of the controller, using the YJBK matrix transfer function Q, can be done without a redesign of the anti-windup part of the controller.

VI. EXAMPLE

Consider the system and controllers from [17] given by:

\[ G_{yu,0} = \frac{10}{s^2 + 10s + 10} \]
\[ G_{yu} = \frac{10}{s^2 - 0.05s + 10} \]
\[ K_{feedback} = \frac{135(s + 5)^2}{s(s + 80)} \]
\[ K_{forward} = \frac{2.5}{s + 2.5} \]

The maximal control input signal is limited to \( u_{\text{max}} = 1.0 \). \( G_{yu,0} \) is the nominal model used for design of the anti-windup controller where \( G_{yu} \) is the system used in the simulations.

A simulation of the system with the nominal controller with and without saturation is shown in Fig. 5. The amplitude of the reference input is 1.1. The feedback system without saturation is closed loop stable whereas it is unstable when input saturation is included.

Fig. 5. Simulation of the system with the nominal controller. Solid line is the system without saturation and dashed line is system with saturation.

Let’s consider three different anti-windup controllers for the system. The anti-windup parts are given by:

\[ Q_s = \begin{cases} 
I - \hat{V} & \text{W-P controller} \\
\frac{k}{G_{yu,0}U} & \text{constant } Q_s \\
-G_{yu,0}\hat{U} & \text{decoupling controller}
\end{cases} \]

The constant \( Q_s = k \) is designed by using a simple optimization. This gives \( Q_s = 10 \). The simulation results of the three anti-windup controllers are shown in Fig. 6.

Fig. 6. Simulation of the system with three different anti-windup controllers. The top plot shows the output from the system y when the W-P controller is applied, the middle plot shows y when \( Q_s = k \) is applied and the lower plot shows y when the decoupling \( Q_s \) is applied.

All three anti-windup controllers results in stable closed-loop systems. The first two anti-windup controllers results in almost the same output with the first one as a little bit better...
than the second controller. The decoupling controller gives an output with a smaller oscillation than the other two controllers. The control signal generated by the anti-windup controllers are shown in Fig. 7. It can be seen from Fig. 7 that the W-P

ccontroller results in a high control signal compared with the two other controllers. \( Q_s = k \) results in reasonable control signal, and the decoupling controller gives a smaller and a more smooth signal control signal than from the two other controllers, but have some spikes.

It is important to point out that the above results depend strongly on the applied coprime factorization of the system and the nominal controller. Other coprime factorizations of the system and nominal controller will give other results, special for the first two anti-windup controllers.

VII. CONCLUSION

A YJBK controller architecture has been applied in connection with input saturation. The YJBK architecture gives an additional YJBK transfer function related to the input saturation part of the system. This makes it easy to include anti-windup in the controller architecture. Standard analysis and design methods for input saturation can be applied.

Relations between the described YJBK architecture and other anti-windup controllers have also been considered. It is shown how the well-known Weston-Postlethwaite architecture and design methods for input saturation can be applied.

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