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Scalable and highly tunable conductive oxide interfaces

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ABSTRACT

Conducting oxide interfaces have attracted considerable attention, motivated by both fundamental science and potential for oxide electronic devices. An important gap for maturing such device technology is scalability and routes to control the electronic properties, which can narrow the device engineering space. Here, we demonstrate and explain the mechanisms of highly tunable conductive oxide interfaces. We synthesized amorphous–crystalline Al₂O₃/SrTiO₃ interfaces using the scalable and industry-compatible atomic layer deposition (ALD) technique. An NH₃ plasma pretreatment is employed in the ALD chamber, and its duration is used as a tuning parameter for the electrical properties, where a span of three orders of magnitude in the sheet resistance is observed at room temperature. For the most conductive sample, our results are comparable to the highest carrier density values reported for all-crystalline oxide interfaces prepared with state-of-the-art epitaxial growth techniques, such as pulsed laser deposition. We pinpoint the origin of conductivity to oxygen vacancies caused by the SrTiO₃ reduction by the NH₃ plasma pretreatment. These results present a simple, scalable, and industry-compatible route for realizing conductive oxide interfaces, with a broad parameter space, offering a versatile and flexible toolkit for oxide device engineering.

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I. INTRODUCTION

Emergent conductivity at oxide interfaces¹ has attracted considerable interest over the past two decades, owing to its rich physics and potential for device applications. This phenomenon is commonly termed a (quasi) 2D electron gas (2DEG), referring to its interfacial nature. The mechanisms behind the formation of oxide 2DEGs at the interface have been attributed to polar discontinuity² and ionic defects. The latter mechanism is commonly attributed to oxygen vacancies,^{1,3–5} while doping caused by interface intermixing^{6,7} and other defects⁸ have also been shown to play a role. Some recent descriptions combine these pictures, invoking interactions between polarity and defects as the mechanism for conductivity.^{9–11} We note that for the sake of simplicity, we broadly and loosely use the term "2DEGs" to describe conductive oxide interfaces, whereas *quasi*-2D or other terms might be more accurate. While research into the fundamentals of oxide 2DEGs persists, there is a growing interest in harnessing this phenomenon toward useful devices. Oxide 2DEGs have been gaining traction for their potential as channels in field effect transistors,^{12–15} gas sensors,^{16,17} spintronic devices,^{18–20} and resistive random access memories (ReRAMs).^{21–25} Maturation of these device applications and the development of new devices require the ability to deterministically control the electrical properties of 2DEGs and to synthesize them in an industrially compatible process. These crucial abilities are needed for enabling such devices, and engineering their properties, such as the on–off ratio and subthreshold slope of field effect transistors, sensitivity of chemical sensors, mitigation of parasitic effects, and other important performance parameters.

Oxide 2DEGs have been most commonly studied at the $LaAlO_3/SrTiO_3$ (LAO/STO) interface,¹ where the former is epitaxially grown on a single crystal substrate of the latter. This material 06 December 2023 08:50:58

system is typically synthesized by pulsed laser deposition (PLD), which can provide excellent crystalline quality and atomic-scale precision. While these features are particularly useful for scientific purposes, PLD has limits for technological applications owing to its low throughput compared to microelectronics-compatible processes and significant costs in operating PLD systems (e.g., procuring excimer laser gas), which limits the scalability and prospects of mass production. To overcome this hurdle, 2DEGs were realized using atomic layer deposition (ALD) of various oxides on STO substrates,^{26–28} followed by an "all-ALD" approach that circumvents the need for STO substrates.^{14,29} ALD is a highly scalable and versatile process,^{30,31} and it is an integral part of any modern microelectronics fabrication flow. As such, ALD provides a route for synthesizing highly scalable and microelectronics-compatible 2DEGs that can push oxide electronics closer to practical realization. However, this approach can suffer from low charge carrier densities in the resulting oxide 2DEGs (leading to high resistances), which are not easily tunable.

Here, we demonstrate and illustrate the mechanisms of a new approach for 2DEG formation with ALD, providing a broad tunability of the electronic properties. This approach is based on an *in situ* pretreatment in the ALD chamber, allowing for three orders of magnitude tunability in the conductivity via the carrier density at room temperature (and at least six orders at 50 K). This method preserves the structure of the interface and the smoothness of the free surface—critical features for oxide electronic devices, such as memristors and transistors. The mechanism of this tunability is pinpointed to a controlled reduction of the substrate surface during pretreatment, where its duration provides a simple and robust tuning parameter.

II. EXPERIMENTAL

Undoped (001) STO substrates $(5 \times 5 \times 0.5 \text{ mm}^3, \text{Crys}^3)$ Tec GmbH) were TiO₂-terminated with the HF method.³² These substrates were heated to 300 °C and subjected to a NH3 plasma treatment performed in the ALD chamber, which induced conductivity at the STO surface by reduction. The NH₃ plasma exposure (~40 mTorr, 300 W) was performed in cycles of 30 s each, and the total time was varied between 0 and 4 min. To avoid reoxidation of the STO surface following atmospheric exposure, all samples were capped with a nominally 4 nm amorphous Al₂O₃ layer, grown by atomic layer deposition (ALD, Ultratech/Cambridge Nanotech Fiji G2). The Al₂O₃ growth was performed using trimethylaluminum (TMA) and water as the precursors³³ (0.1 s of TMA pulse, 10 s of purge, 0.3 s of water pulse, and 5 s of purge), keeping the substrate temperature at 300 °C. During the initial temperature ramping, the chamber pressure is ~15 mTorr (with several short Ar purges reaching a peak of ~7 Torr at a cycle of 3 min). The Al₂O₃ layer thickness is found to agree with the nominal values using x-ray reflectivity. Each plasma exposure and subsequent Al₂O₃ deposition were performed on two batches of substrates. The first batch includes four substrates simultaneously, where one representative sample from each process was selected for full analysis. See the full dataset (Table S1 and Fig. S2), details, and discussion of the effect of light exposure in the supplementary material. The second batch included only one sample in each run, which was used for x-ray photoemission spectroscopy analysis. In

addition, a 10 nm Al_2O_3 layer was grown under the same conditions, with consistent electrical properties vs the 4 nm layers.

The surface topography was studied with atomic force microscopy (AFM, Asylum MFP-3D Infinity) in tapping mode. A transmission electron microscope (TEM) cross section sample was prepared from a 10 nm Al₂O₃ sample using a dual-beam focused ion beam (FIB, Helios Nano-Lab G3 FEI) microscope, and imaging was performed using a high-angle annular dark-field (HAADF) detector in scanning TEM (STEM) mode (FEI Titan 80-300 kV FEG-S/TEM). Magneto-transport measurements were performed in the van der Pauw geometry by depositing Au/Ti (100/5 nm) contacts (e-beam evaporation) after gently scratching the wafer corners with a scriber to ensure contact with all the layers. These contacts were bonded with Au wires. Hall and magnetoresistance (MR) measurements were performed using Physical Property Measurement System (PPMS, Quantum Design DynaCool) system in resistivity mode. The fixed measurement current was selected to be between 0.01 and 10 μ A depending on sample conductivity. The Hall measurement includes high-field sweeps (-10 to 10 T) at 300 and 4 K (or the lowest measured temperature, depending on sample conductivity) and low-field sweeps (-1 to 1 T) every 30 K. The magnetoresistance measurements were also performed under high-field conditions, every 90 K until the lowest temperature, depending on the sample resistivity and the PPMS measurement limits.

X-ray photoelectron spectroscopy (XPS) measurements were carried out using a monochromated Al K α (1486.6 eV) x-ray source and a Scienta Omicron EW4000 electron energy analyzer using 100 eV pass energy. The sample normal was aligned to the spectrometer axis to ensure a large information depth. Data were fit with the CasaXPS software after a Shirley background³⁴ subtraction.

III. RESULTS AND DISCUSSION

The surface topography of a 4 nm Al₂O₃ film grown on an STO substrate after 2 min of NH₃ plasma exposure exhibits long-range atomically flat terraces [Fig. 1(a)], indicating that the surface roughness is below the ~0.4 nm STO step height. The cross section STEM image [Fig. 1(b)] shows an abrupt interface between a 10 nm Al₂O₃ layer and the STO, for the most aggressive treatment—4 min of NH₃ plasma exposure, indicating that the NH₃ treatment does not result in significant structural changes in the STO surface. The slight crystalline contrast in the Al₂O₃ layer is ascribed to (unintentional) crystallization under the TEM beam.²⁸ The films are otherwise amorphous, within the sensitivity of x-ray diffraction (Fig. S1 and details therein), in agreement with our previous experience with comparable layers.^{35,36}

The temperature dependence of the sheet resistance for six different NH₃ plasma durations, ranging from 30 s to 4 min, is presented in Fig. 2(a). Control samples without a plasma treatment were insulating beyond our measurement capability.³⁵ At room temperature, we observe a monotonous decrease in the sheet resistance as the plasma duration increases, ranging across three orders of magnitude. Previous studies of 2DEG systems with ALD Al₂O₃^{26,37} and LAO/STO⁴ reported typical sheet resistances in the range of 10^4 – $10^5 \Omega$ /sq. A larger range and tunability were demonstrated via control of the growth oxygen environment^{1,3} and via post-growth



FIG. 1. Microstructural analysis of Al_2O_3 -SrTiO₃ structures. (a) AFM image (5 × 5 μ m²) of a 4 nm Al_2O_3 layer on the (001) SrTiO₃ surface with 2 min of NH₃ plasma exposure. (b) Cross-sectional HAADF-STEM micrograph of a 10 nm Al_2O_3 layer with 4 min of NH₃ plasma exposure.



FIG. 2. Temperature dependence of (a) the sheet resistance and (b) the sheet carrier (N_s) density for varying NH_3 plasma durations (the lines are a guide to the eye). Samples with no plasma exposure exhibited resistivities beyond the measurement limit.

annealing at various oxygen pressures.⁵ Recently, a two-order magnitude tunability at room temperature was demonstrated by doping³⁸ with a PLD process. Here, we report continuous tunability of the sheet resistance from 10^2 to $10^5 \Omega/sq$ at room temperature. This large tunability demonstrates robust control of the interface conductivity using a simple pretreatment, which is part of a scalable deposition process. Modern ALD systems can process dozens of large wafers in a single run, providing a highly scalable and microelectronics-compatible route for oxide interfaces with tailormade electronic properties. At low temperatures, this tunability range increases to at least six orders of magnitude [Fig. 2(a)].

The temperature-dependent sheet resistance further shows a metal-insulator transition (MIT) between 40 and 50 s of plasma pretreatment, as evident by the crossover from a negative (insulating) R_s -T slope until 40 s to a positive (metallic) slope from 50 seconds and above. Such a carrier density-dependent MIT is consistent with bulk SrTiO₃³⁹ and SrTiO₃-based 2DEG systems.^{3,5,11,40,41}

Sheet carrier densities and mobility values were extracted by Hall measurements, from the transverse resistance vs the magnetic field behavior⁴² (R_{xy} –B). Other than a single data point (4 min exposure, measured at 4 K, to be discussed later), all the R_{xy} –B data exhibited linear relations and were fitted accordingly as a single conductive channel.⁴³ The carrier density and mobility trends [Figs. 2(b) and S3] confirm the expectation that the conductivity tunability originates from the carrier density: the longer plasma exposure duration increases the degree of STO reduction, which is the source of the vacancy-induced electrons. Plasma durations longer than 4 min were not attempted in this work, but the effect of extended plasma is expected to saturate at some point, and it might result in the formation of different types of defects and even structural changes in the STO surface. The carrier density

of the metallic regime is largely temperature-independent,^{39,41,44} whereas the insulating region exhibits a thermally activated behavior.

The extracted mobility values exceed $10^4 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at low temperatures for the metallic samples (Fig. S3). These values are comparable to those for $\text{AlO}_x/\text{STO}^{43,45}$ and one order of magnitude lower than the highest reported values in the literature for γ -Al₂O₃/STO.^{46,47} We ascribe the high mobility here to the chemical process of vacancy formation, which preserves the microstructural quality of the STO surface (Fig. 1). By contrast, in several reports, kinetic damage from the deposition (typically PLD) is the mechanism for vacancy formation, which can reduce the mobility. Therefore, the potential of the ALD technique to induce even higher mobilities is evident, all the while being scalable and suitable for mass production.

We consider the interpretation of the low-temperature Hall data and the possibility of multichannel conduction. This is done by looking for its fingerprint of nonlinear R_{xy} –B behavior.^{20,48,49} After examining the low-temperature two-channel fits for all the conductive samples (Fig. S4 and discussion therein), such a behavior is found to be distinct only for the 4 min sample at 4 K. The origin of splitting into two types of carriers (chaneels) is due to selective orbital occupation; the Ti $3d_{xy}$ orbital is filled first and donates the first type of carriers. Once the $3d_{xy}$ is partially filled to a critical value, the higher-energy d_{xz} and d_{yz} orbitals begin to fill.⁵⁰ While anomalous Hall effect (AHE) is another possible source for a nonlinear R_{xy} –B behavior (due to possible Ti³⁺ magnetism), the absence of a Kondo resistivity upturn⁵¹ in the R_S–T behavior [Fig. 2(a)] implies that AHE does not have a significant contribution here.

The two-channel Hall analysis (4 min, 4 K) yields two channels with a similar electron density of ~ 2.5×10^{15} cm⁻² and dissimilar mobility values of 8 × 10² and 1.4 × 10⁴ cm² V⁻¹ s⁻¹ [Fig. 2(c)]. Few studies have shown sheet carrier densities exceeding 10¹⁵ cm⁻², ⁵²⁻⁵⁶ making the present result one of the highest reported to date. All previous reports of high carrier density systems were reported for epitaxial interfaces (mostly fabricated by PLD). Altogether, Fig. 2 exhibits one of the most tunable oxide interfaces ever reported, which can reach carrier densities that are comparable to the record reports; these interfaces are fabricated using a simple, scalable, highly tunable, and microelectronics-compatible material and process.

The results are consistent with the expected mechanism of reduction of the STO surface and creation of oxygen vacancies by the plasma treatment. The higher mobility in the present case is ascribed to the structural integrity of the topmost STO layers [Fig. 1(b)], owing to the low-energy nature of ALD vs the high kinetic energy of the more common PLD process. Moreover, the high mobility values suggest that it is a (quasi-) 2D system, and the 2DEG is centered in close proximity to the Al₂O₃/STO interface.⁴⁶

The temperature-dependent carrier density behavior of the insulating samples (t = 30, 40 s) indicates that the carriers are thermally activated. This is confirmed by an Arrhenius plot (Fig. 3), where fits above 150 K exhibit a linear behavior with activation energies of \approx 54 and 31 meV for 30 and 40 s plasma durations, respectively. We note that these values represent a range, with the exact slope value depending on the specific choice of temperature range



FIG. 3. Arrhenius plot of the sheet carrier density (N_s) vs the inverse temperature for the insulating samples of 30 and 40 s plasma exposure. The dashed lines represent the linear fits in the 300–150 K range.



FIG. 4. (a) Ti $2p_{3/2}$ XPS spectra and fitting envelope of Al₂O₃–STO structure with different NH₃ plasma exposure durations. The spectra were obtained underneath a 4 nm Al₂O₃ layer. The inset is an example of a three-component (4+, 3+, and 2+) fit of the 4 min sample. (b) The relative area of the fit components and the sum of 3+ and 2+ areas obtained from the Ti $2p_{3/2}$ spectra vs the NH₃ plasma pretreatment duration (the lines are a guide to the eye).

of the Arrhenius fit, where the deviation from linearity is ascribed to additional thermal processes. These values are in good agreement with previous reports of oxygen vacancy-induced donor levels of 30 meV for ALD-Al₂O₃/STO,²⁶ 25 meV for reduced STO,⁵⁷ and 18–189 meV for the related ALD-Al₂O₃/TiO₂ system²⁹ (the energy represents the depth of the donor below the conduction band). Altogether, Fig. 3 supports the NH₃ plasma-induced oxygen vacancy picture, with electron activation energy range in agreement with previous reports.

To gain further insights into the 2DEG formation mechanisms, XPS analysis of the buried Al₂O₃/STO interface was employed. The XPS spectra of Ti 2p_{3/2} consist of a major Ti⁴⁺ oxidation state (aligned to 458.4 eV) and a broad shoulder at lower binding energies (BEs), which is ascribed to Ti^{3+} and Ti^{2+} states^{26,29,58-61} [Fig. 4(a)]. No constraints were applied to the fit in order to capture possible variations in the peak shapes; these were found to be negligible, and the peak offsets are in agreement with the literature⁶² (see Table S2 for the full details). The relative area of the reduction-induced peaks $(Ti^{3+} and Ti^{2+})$ increases with the NH₃ pulse duration [Fig. 4(b)]. The absence of these features in the spectrum of the insulating sample without an NH₃ treatment confirms that the plasma treatment induces oxygen vacancies near the interface and that this is the mechanism behind the interfacial conductivity.^{26,29} Furthermore, no nitrogen signal was observed in the N 1s spectral region (Fig. S5), which indicates that no nitrogen has been incorporated in the films, at least not in concentrations appreciable by XPS, which is below a percent level.63

IV. CONCLUSION

We synthesized Al₂O₃-STO interfaces using ALD and performed an NH₃ plasma pretreatment immediately before Al₂O₃ deposition. This procedure does not result in observable structural changes. The plasma duration was employed as a tuning parameter for the electronic properties of the conductive interface. Without plasma exposure, the interface is insulating, and between 30 s and 4 min of exposure leads to three orders of magnitude increase in conductivity (and six orders at 50 K). The origin of the conductivity increase is via an increase in the carrier density, without a significant effect on the mobility. We identify and explain this mechanism behind the conductivity tuning to be a reduction of the STO surface during pretreatment, a monotonous increase in suboxidized Ti features in XPS with increasing plasma exposure times. Altogether, our results highlight a scalable approach for synthesizing highly tunable oxide interfaces, driving oxide electronics closer toward mass-production capabilities and practical realization.

SUPPLEMENTARY MATERIAL

The supplementary material includes x-ray diffraction of an Al₂O₃ layer, room temperature electrical properties of the entire set of samples, discussion on the effect of light, data and discussion of the mobility, fiting and analysis of the two-channel mode magneto-transport and Hall data, XPS fitting parameters, and scans of the N 1s spectral region. A separate .xlsx file includes all the data used to prepare the figures of the paper and additional raw data.

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AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

Dana Cohen-Azarzar: Data curation (lead); Formal analysis (equal); Methodology (equal); Writing – original draft (equal); Writing – review & editing (equal). **Maria Baskin**: Data curation (equal); Methodology (equal). **Andreas Lindblad**: Data curation (equal); **Felix Trier**: Conceptualization (equal); Formal analysis (equal); Methodology (equal). **Lior Kornblum**: Conceptualization (lead); Funding acquisition (lead); Methodology (equal); Supervision (lead); Writing – original draft (equal); Writing – review & editing (equal).

DATA AVAILABILITY

The data that support the findings of this study are available within the article and its supplementary material.

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