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Investigation of Top/Bottom Electrode and Diffusion Barrier Layer for PZT Thick Film MEMS Sensors

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Abstract—In this work screen printed piezoelectric Ferroperm PZ26 lead zirconate titanate (PZT) thick film is used for two MEMS devices. A test structure is used to investigate several aspects regarding bottom and top electrodes. 450 nm ZrO_2 thin film is found to be an insufficient diffusion barrier layer for thick film PZT sintered at 850°C. E-beam evaporated Al and Pt is patterned on PZT with a lift-off process with a line width down to 3 μm . The roughness of the PZT is found to have a strong influence on the conductance of the top electrode.

I. INTRODUCTION

Micro-electromechanical systems (MEMS) utilizing the piezoelectric effect have gained increasing attention lately. The purpose of this paper is to investigate bottom and top electrode formation for MEMS devices utilizing the piezoelectric effect of screen printed $\text{Pb}(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$ (PZT) materials.

Fig. 1 shows a schematic drawing of the two devices of interest, a) a tri-axial accelerometer [1] and b) a piezoelectric Micromachined Ultrasonic Transducer (pMUT). Beams or membranes are created by a deep plasma etch and SOI wafers are used to ensure a uniform beam or membrane thickness. For both device designs high resolution patterning of the top and bottom electrodes are crucial. The electrode thickness has an influence on the mechanical properties of the MEMS device and should be minimized.

PZT thick films have a high coupling factor and shows small temperature dependence. Screen printing of PZT on Si substrates [1] has shown promising results and thicker films are obtainable compared to the sol-gel thin film technique. Thick films result in higher sensitivity and are preferred for

MEMS accelerometers and pMUT scan heads utilizing the d_{31} mode. Screen printing of PZT results in new fabrication challenges with respect to the electrodes compared to thin film processing [2,3].

Thick film PZT requires a high sintering temperature of at least 850°C and a better diffusion barrier layer between Si and the PZT is therefore needed compared to thin film PZT. Ti/Pt is a good conducting diffusion barrier layer [2]. For the device designs shown in Fig. 1 an insulating barrier layer is preferred as it allows patterning of the bottom electrode. ZrO_2 has successfully been used for thin film PZT [3] and is tested for screen printed PZT in this paper. It is important to note that the type of PZT used in this work is PZ26 from Ferroperm Piezoceramics A/S. PZ26 is a hard doped PZT material.

Patterning of the top electrode using an etch-back of the metal is challenging as the properties of the PZT typically also are affected [4], thus a lift-off based technique is employed in this work. A rougher PZT surface due to the larger grain size of thick films demands a good top electrode coverage. A comparison is made between the resolutions of a screen printed Au top electrode and an e-beam evaporated top electrode patterned with a lift-off process. Both Al and Pt are tested with the lift-off approach and compared with respect to the deposition process and material properties. Screen printed top electrodes are in the range of a few micrometers [5] where e-beam evaporated are in the range of a few hundred nanometers. The optimal thickness of the latter is found by introducing a roughness factor indicating the change in conductance due to the roughness of the PZT surface.

Figure 1. a) Schematic of an accelerometer with a seismic mass suspended by two beams. b) Schematic of a pMUT with PZT thick film and patterned top and bottom electrode.

II. BOTTOM ELECTRODE

In order to create a patterned bottom electrode it is necessary to coat the silicon substrate with an insulating diffusion barrier layer. If no barrier layer is present, interdiffusion of Pb and Si will destroy the piezoelectric properties of the PZT. Previously it has been reported that 60 nm of ZrO_2 is a functional diffusion barrier layer for PZT thin films sintered at 700°C [3]. Thus it is attempted to use a ZrO_2 film for PZT thick films. The ZrO_2 film is deposited by a sol-gel spin on method. The precursor sol is made of zirconium n-propoxide stabilized by acetylacetone and dissolved in isopropanol. Several layers are spun on Si wafers at 3000 RPM with intermediate baking steps at 350°C before the final anneal at 900°C . Up to 10 layers have been deposited for a total thickness of 450 nm, the ZrO_2 layer is deposited on top of a 500 nm SiO_2 layer. This has however proven to be insufficient as a diffusion barrier layer. The most likely reason is that the high sintering temperature of 850°C for one hour causes a diffusion of Pb and Si across the ZrO_2 layer. As no insulating diffusion barrier layer has been found the focus of this work is on the top electrode.

III. TOP ELECTRODE

Top electrodes are formed both by screen printing and by e-beam evaporation followed by lift-off. As the evaporated metal layers have a thickness down to 100 nm the roughness of the PZT becomes of great importance.

A. PZT Roughness

The PZT layer is screen printed on Si substrates with a $\text{SiO}_2/\text{Ti}/\text{Pt}$ layer with a thickness of 500/50/500 nm, this serves as a diffusion barrier layer and bottom electrode. The PZT is screen printed through a 77T polyester mesh mask which has 77 lines per inch (30 lines per centimeter). The wires in the mask are 70 μm thick. The roughness of the screen printed PZT is evaluated with a Tencor P-1 stylus profiler. A typical scan of a 30 μm thick screen printed PZT layer is seen in Fig. 2. The large features separated a few hundred micrometers is due to the wires in the polyester mesh mask. The RMS roughness is measured to be 1.6 μm based on 5 mm long scans. This roughness is higher than for typical PZT thin films and thus it is more difficult to do

planar processing on thick film PZT compared with thin film PZT.

B. Metal Deposition Techniques

Metallization for the top electrode can be done by screen printing a gold paste through a mask. This technique has a resolution of roughly 100 μm and a metal thickness of 5-10 μm . The roughness is even greater than the underlying PZT, see Fig. 3. Electrical connectors with a line width of 100 μm creates large parasitic capacitances, and the thick screen printed electrode has a large influence on the mechanical behavior of the device; this influence is diminished by making a thinner e-beam evaporated top electrode.

Figure 2. Stylus profiler scan of a screen printed PZT layer with a thickness of 30 μm . The RMS roughness is calculated from such scans to be 1.6 μm .

Figure 3. Profile scan of a 200 μm wide screenprinted Au electrode. The roughness of the electrode is greater than the underlying PZT.

E-beam evaporation is done in a Wordentec QCL800 Metal Evaporator. The deposited metal is patterned with a lift-off technique. Both Pt and Al are tested as top electrode materials. Lift-off of metal is usually done with a negative tone photoresist due to the inward sloping profile, but can be difficult to control when using a thick ($\sim 5 \mu\text{m}$) resist. In this case, the resist has to be thick enough to cover the roughness of the PZT layer. Thus, also a thicker, positive tone photoresist is tested. The negative resist has a thickness of 4.2 μm and the positive resist has a thickness of 9.5 μm . As positive resist, AZ4562 is used and as a negative resist AZ5214E Image Reversal resist is used.

Previously adhesion problems have been reported when immersing the PZT in photoresist developer (Microposit MF322), resulting in the PZT to peel off [4]. In our case a AZ351B developer does not seem to affect the PZT.

It proved to be difficult to lift off the metal from the negative process, requiring significantly more ultrasonic agitation than when using the positive process. It is believed that the thinner negative tone resist is not thick enough to effectively cover the PZT surface during metal deposition.

The best results are obtained when using a positive lithography process, even though the resist profile has outward sloping sidewalls. The metal covers most of the sloped sidewall of the photoresist but there is a small gap for the acetone to penetrate, see Fig. 4. With the positive lithography process it has been possible to produce 3 μm wide Al wires with a thickness down to 100 nm and a length of 20 mm on 30 μm thick PZT layers, see Fig. 5.

Figure 4. SEM image of a patterned, positive tone photoresist on PZT with 500 nm Al deposited on the photoresist for lift-off. The metal is seen to cover most of the sloped sidewall of the photoresist.

Figure 5. SEM images of a 1000 nm (a) and 200 nm (b) thick Al top electrode on PZT thick film fabricated with a lift-off process. Line widths down to 3 μm have successfully been fabricated on the rough PZT surface.

A direct comparison between the results from the positive and the negative process are seen in Fig. 6. Here microscope images of a 20 μm wide Pt wire from both a positive and a negative process are shown. It is clear that the line definition is better from the positive process. The poor line definition from the negative process is most likely an effect from the coverage problems of the resist because it is too thin compared to the roughness of the PZT.

After deposition of Pt the resist would often appear to have developed cracks. This is believed to be due to poor thermal conduction of the relatively thick photoresist and PZT layer. This sort of damage to the resist has not been

seen when depositing Al, which has a significantly lower melting point than Pt and thus transfers less heat to the substrate during deposition. With regards to top electrode deposition Al is found to be better than Pt.

Figure 6. Comparison of 20 μm wide Pt wires fabricated on PZT with a) positive and b) negative tone photoresist.

IV. TOP ELECTRODE THICKNESS

The optimal thickness of the top electrode is a compromise between good conductance and low mechanical influence. In order to quantify the conductance of the lift-off top electrode a test mask with wire length, l , ranging from 1 mm to 20 mm and wire width, w , from 3 μm to 100 μm are used.

The resistance, R , of a wire deposited on the PZT will be greater than the resistance of a similar wire on a flat substrate due to the roughness of the surface. This increase compared with an equivalent wire on a smooth surface relates how the roughness affects the conductance of the top electrode. The resistance of a long and narrow wire on the PZT surface is given by,

$$R = x \frac{\rho l}{wh} \quad (1)$$

where ρ and h are the resistivity of the metal and the top electrode thickness respectively. The change of resistance due to the roughness of the PZT surface is expressed by the roughness factor, x .

As the top electrode thickness decreases, the roughness of the PZT has a larger impact on resistance and the roughness factor increases. For increasing top electrode thickness the roughness factor goes asymptotically to one. A roughness factor of one corresponds to a perfect wire on a completely smooth surface. Both Pt and Al are tested as top electrode materials.

Wires with thicknesses ranging from 100 nm to 1000 nm are deposited. Fig. 7 shows the resistance as a function of the inverse wire width for four different thicknesses of Al wires. The good linear fits ($R > 0.98$) indicate the expected ohmic behavior of the wires.

Figure 7. Resistance as a function of the inverse wire width, w^{-1} , is shown for different Al top electrode thicknesses, h . A linear, ohmic behavior is seen and the roughness factor is a function of the slope of the linear fits.

The roughness factor is given as a function of the slope of the lines in Fig. 7. The roughness factor is decreasing as a function of thickness as expected, see Fig. 8. A thickness of 100 nm gives a roughness factor of 4.6 which means that the electrode is 4.6 times less conducting compared to an electrode on a smooth surface. The roughness factor should theoretically be independent of the electrode material although Fig. 8 generally shows a slightly higher value for Pt compared with Al. This is presumably related to the difficulties in the lift-off process of Pt as explained earlier.

Figure 8. Roughness factor, x , as a function of metal thickness for thicknesses between 100 and 1000 nm.

As demonstrated, the resistance of wires deposited on PZT is not simply linearly dependent on metal thickness due to the roughness of the PZT, so this aspect has to be considered when designing PZT thick film based devices.

Increasing the electrode thickness has a negative effect on the mechanical behavior of the MEMS devices. For instance,

a top electrode with a thickness of 600 nm on a Si beam with a thickness of 40 μm in an accelerometer design as shown in Fig. 1, results in an decrease in beam deflection of 2.4% and 5.7% for Al and Pt, respectively. The effect on the mechanical behavior can therefore not be neglected.

A top electrode thickness of 600 nm seems very high compared to what have been presented previously [2] but as Fig. 8 shows, the high surface roughness demands thicker top electrodes. As long as the top electrode is deposited after the sintering process almost all metals are candidates. At first Al seems to be preferable compared with Pt due to the lower Young's modulus and less influence on the mechanical behavior. For piezoelectric MEMS devices with a sandwich structure of different layers (see Fig. 1) the coefficients of thermal expansion, CTE , should also be considered. Al and Pt have 24 $\mu\text{m}/(\text{m}^\circ\text{C})$ and 9.2 $\mu\text{m}/(\text{m}^\circ\text{C})$ respectively and Si and PZT have 2.6 $\mu\text{m}/(\text{m}^\circ\text{C})$ and 2.0 $\mu\text{m}/(\text{m}^\circ\text{C})$, respectively. Considering the CTE , Pt is the preferred choice.

V. CONCLUSION

Using screen printed PZT thick films instead of thin films results in more critical fabrication processes of both the bottom and top electrodes. In order to pattern the bottom electrode 450 nm ZrO_2 as an insulating diffusion barrier layer is found to be insufficient for screen printed PZT. Al and Pt are tested as top electrode materials which both have advantages and disadvantages. A factor describing the decrease in conductance of the top electrode due to the surface roughness is introduced.

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