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An Integrated Three-phase Transformer for Partial Parallel Dual Active Bridge Converter

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Abstract—In this paper, an integrated three-phase transformer is utilized in the partial parallel dual active bridge (PPDAB) converter. Due to the symmetrical structure, the flux generated by the primary windings is equally distributed to the three outer legs. Given the same number of turns of the windings in each parallel modules, the ac currents can inherently balance in the parallel modules. In order to enlarge the leakage inductor to serve as the interfacing ac inductor in the PPDAB converter, the coupling between the primary windings and secondary windings is weakened by placing the primary and secondary windings on the center and outer legs, respectively. Compared to the discrete three-phase transformers, the winding loss will also decrease with the proposed structure. A 1kW 400V/50V 500 kHz prototype with an efficiency of 96.72% is built and tested to verify the theoretical analysis.

Index Terms— DAB, dc-dc, integrated magnetics, three-phase transformer

I. INTRODUCTION

Dual active bridge (DAB) converter is widely used in energy storage systems, automotive applications, smart transformers, etc., due to its attractive characteristics such as bidirectional power flow capability, symmetrical configuration and zero voltage switching (ZVS) [1]-[8].

In [6], an overview of the DAB converter for high-frequency-link power conversion systems was given.

In [7], a novel neutral point clamped (NPC) DAB converter with a blocking capacitor is proposed for energy storage systems. By inserting a blocking capacitor in the primary loop of the NPC DAB converter, the proposed topology adapts to wide output voltage range applications.

In [8], one DAB-based architecture, quadruple active bridge (QAB) converter is proposed to be used as a basic module of a modular three-stage solid-state transformer (SST) or smart transformer. A 20kW prototype of the smart transformer was built to demonstrate the high potential of the QAB converter on efficiency and cost.

Achieving high power density is also emerging as a goal in power electronics applications. Passive components such as inductor, capacitor, and transformer often occupy the vast majority of the volume of the converter. Besides, increasing the switching frequency to reduce the size of passive components, integrated magnetics are also widely used in power electronic converters [9]-[15].

Moreover, integrated magnetic techniques are widely applied to high power applications.

In [14], a matrix transformer structure was proposed for a 1 MHz, 380V/12V, 800W LLC converter. The concept of flux cancelation is used to reduce core loss and core size.

In [15], a 6-layer PCB winding three-phase transformer is reported. The leakage inductor can be controlled to the desired value by adjusting the reluctance of the additional center post to serve as the resonant inductor. However, the additional center post will also increase the core loss.

In this paper, a new integrated three-phase transformer is proposed for the partial parallel dual active bridge (PPDAB) converter, shown in Fig. 1. The PPDAB converter was first proposed and researched in [4]. Based on the dual active bridge (DAB) topology, the transformer windings on the high-voltage (HV) side are connected in series; on the other hand, the full-bridges units on the low-voltage (LV) side are connected in parallel, making the voltage gain and the transmission power significantly increased.

For the PPDAB converter, interfacing inductors are needed in the ac link in order to enable regulation of power flow between the HV and LV sides. Therefore, the proposed three-phase transformer integrates not only three transformers but also the ac inductor into one magnetic structure so that further reduces the size and volume of magnetic components. Moreover, the winding loss can be reduced accordingly with the proposed magnetic integration method. The symmetrical structure leads to the flux generated by the primary windings equally distributed to the three outer legs. Given the same number of turns of the windings in each parallel modules, the high-frequency ac currents are balanced on the LV side.

Fig. 1. Topology of the partial parallel dual active bridge converter
This paper is organized as follows. After the introduction, Section II analyzes the operation of the PPDAB converter. Section III gives the detailed analysis on the magnetic integration of three-phase transformers and ac inductors, and compares it with the discrete three-phase transformer, which is comprised of three sets of UI core. Section IV presents the experimental results on the 1kW three-phase PPDAB converter to verify the converter’s analysis and operation with the newly proposed integrated three-phase transformer. Finally, conclusion is given in Section V.

II. PARTIAL PARALLEL DUAL ACTIVE BRIDGE DC/DC CONVERTER

The typical waveforms of the PPDAB converter with single-phase-shift (SPS) control are shown in Fig. 2, where \( \phi \) represents the phase-shift angle as a percentage of the switching period \( T_s \), \( f_s \) is the switching frequency, and \( L_{ac} \) is the total leakage inductance referred to the HV side. Moreover, the theoretical analysis below is based on the following assumptions: 1) All the switches are ideal. 2) The input voltage \( V_{in} \) and output voltage \( V_{out} \) are constant.

\[ \text{[t}_0, t_1] \]

In this subinterval, the voltage across the ac inductor is \( V_{in} + 3nV_{out} \). The ac inductor current increases linearly.

\[ i_{ac} (t_1) = i_{ac} (t_0) + \frac{V_{in} + 3nV_{out}}{L_{ac}} \cdot \phi T_s \quad (1) \]

\[ \text{[t}_1, t_2] \]

In this subinterval, the voltage across the ac inductor is \( V_{ac} - 3nV_{out} \).

\[ i_{ac} (t_2) = i_{ac} (t_1) + \frac{V_{ac} - 3nV_{out}}{L_{ac}} \left( \frac{1}{2} - \phi \right) T_s \quad (2) \]

Similarly, \( i_{ac}(t_3) \) and \( i_{ac}(t_4) \) can be derived as expressed in (3) and (4),

\[ i_{ac} (t_4) = i_{ac} (t_3) - \frac{V_{ac} + 3nV_{out}}{L_{ac}} \cdot \phi T_s \quad (3) \]

\[ i_{ac} (t_4) = i_{ac} (t_1) - \frac{V_{ac} + 3nV_{out}}{L_{ac}} \left( \frac{1}{2} \right) T_s \quad (4) \]

Due to the symmetry of the ac inductor current, \( i_{ac}(t_0) = -i_{ac}(t_2) \). So \( i_{ac}(t_0) \) can be derived,

\[ i_{ac} (t_0) = -\frac{V_{ac} + 3nV_{out}}{4L_{ac}} (4\phi - 1) T_s \quad (5) \]

The average current of ac inductor \( L_{ac} \) in a half period is expressed in (6).

\[ \bar{I} = \left[ i_{ac} (t_0) + i_{ac} (t_1) \right] \cdot \phi + \left[ i_{ac} (t_1) + i_{ac} (t_2) \right] \left( \frac{1}{2} - \phi \right) \]

\[ = \frac{3nV_{out}}{L_{ac}} \phi (1 - 2\phi) T_s \quad (6) \]

Hence, the PPDAB converter’s steady-state power equation can be calculated,

\[ P = V_{in} \cdot \bar{I} = \frac{3nV_{out}}{f_s L_{ac}} \phi (1 - 2\phi) \quad (7) \]

Assume the load \( Z_{load} \) is fixed, the voltage gain can be derived as (8).

\[ \frac{V_{out}}{V_{in}} = \frac{3nZ_{load}}{f_s L_{ac}} \phi (1 - 2\phi) \quad (8) \]

Compared to the DAB converter, the PPDAB converter has the following advantages:

- The voltage gain is proportional to the number of paralleled modules.
- The ac current balancing between the parallel modules is inherently ensured by the series connection of the windings on the HV side.
- In addition to single-phase-shift (SPS), the PPDAB converter can be regulated by adding phase shifts among the parallel modules.

III. INTEGRATED THREE-PHASE HIGH-FREQUENCY TRANSFORMER

A. Integrated three-phase transformer

As shown in Fig. 1, the three-phase transformer is a key component to achieve galvanic isolation and voltage converting. In order to improve the performance of the PPDAB converter, a newly integrated three-phase transformer is proposed. The structure is shown in Fig. 3(a) and the equivalent circuit is depicted in Fig. 3(b). The dash lines in Fig. 3(b) means the coupling between the windings. The coils of the HV side, winding-P are wound around the center leg. In addition, the coils of the three parallel modules, winding-A, winding-B, winding-C are wound around three outer legs, respectively. The cross-sectional area of the center leg is equal to the total area of the three outer legs to ensure the same flux density along the magnetic paths.
B. Analysis on ac current balancing

The reluctance model of the proposed integrated three-phase transformer is shown in Fig. 4. Where \( F = NI \) is the magneto motive force (MMF), \( N \) is the number of turns in the coil and \( I \) is the current through the circuit. \( \mathcal{R} = l / (\mu A) \), where \( l \) is the length of flux path, \( \mu \) is the permeability of the material and \( A \) is the cross-sectional area. Due to the symmetrical structure, the following assumptions are made:

- The reluctances of three outer legs are the same. Define: \( \mathcal{R}_d = \mathcal{R}_b = \mathcal{R}_c = \mathcal{R}_s \)
- The reluctances between two arbitrary legs are the same. These reluctances are relative to the thickness of the air gap.

Define: \( \mathcal{R}_{dd} = \mathcal{R}_{bb} = \mathcal{R}_{cc} = \mathcal{R}_{ss} \)

As shown in Fig. 4, the reluctance model of the center leg is split into three parts. Combine Ampere's Circuit Law, (9) can be obtained.

\[
\begin{align*}
\phi_a &= \frac{N_a I_a}{3R_p + R_s} + \frac{N_d I_d}{3R_p + R_s} + \frac{(2N_a I_a - N_d I_d - N_c I_c) - 9R_p^2}{3(R_p + R_s)(9R_p R_s + 6R_p R_{ss} + 2R_s R_{ss})} \\
\phi_b &= \frac{N_b I_b}{3R_p + R_s} + \frac{N_d I_d}{3R_p + R_s} + \frac{(2N_b I_b - N_d I_d - N_c I_c) - 9R_p^2}{3(R_p + R_s)(9R_p R_s + 6R_p R_{ss} + 2R_s R_{ss})} \\
\phi_c &= \frac{N_c I_c}{3R_p + R_s} + \frac{N_d I_d}{3R_p + R_s} + \frac{(2N_c I_c - N_d I_d - N_a I_a) - 9R_p^2}{3(R_p + R_s)(9R_p R_s + 6R_p R_{ss} + 2R_s R_{ss})} \\
\phi_p &= \frac{N_p I_p}{3R_p + R_s} + \frac{N_c I_c}{3R_p + R_s} + \frac{(2N_p I_p - N_c I_c - N_d I_d) - 9R_p^2}{3(R_p + R_s)(9R_p R_s + 6R_p R_{ss} + 2R_s R_{ss})}
\end{align*}
\]

where \( \phi_p \) is the total flux in the center leg, and \( \phi_a, \phi_b, \phi_c \) are the flux in the three outer legs, respectively.

Due to the symmetrical structure and the same switching patterns of the three parallel modules, the following equation can be derived:

\[
\phi_a = \phi_b = \phi_c = \frac{1}{3} \phi_p \quad (10)
\]

Combining (9) and (10), (11) can be obtained.

\[
N_a I_a = N_b I_b = N_c I_c \quad (11)
\]

Therefore, it can be seen that when the numbers of turns of winding-A, winding-B, winding-C are kept the same, the ac currents in each parallel module will be balanced.

C. Leakage inductance

By placing the primary and secondary windings on the center leg and outer legs, respectively, the leakage inductance of the transformer can be increased. In order to ensure the leakage inductor is large enough to serve as the ac inductor in the PPDAB converter, the finite element analysis (FEA) is used to design the windings structure of the transformer.

D. Comparison between the integrated three-phase transformer and discrete three-phase transformer

Winding loss is frequency-dependent due to the skin effect and proximity effect. Both effects cause the current unevenly distributed in the cross section of the conductor and cause a high winding resistance at high frequency.

The windings of the integrated three-phase transformer and the discrete three-phase transformer are depicted in Fig. 5.

With the FEA method, the winding loss can be simulated. In order to make a fair comparison, the parameters of the two three-phase transformers are listed in Table I and the simulation results are listed in Table II. From this comparison, we can conclude that the proposed integrated three-phase transformer has a lower winding loss.
Fig. 5. Compassion of two three-phase transformer structures. (a) discrete and (b) integrated.

TABLE I
PARAMETERS OF THE TWO THREE-PHASE TRANSFORMERS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>U-37.5-8.525-18.5</td>
<td>Core dimensions(integrated)</td>
<td></td>
</tr>
<tr>
<td>U-32.6-8.525-18.5</td>
<td>Core dimensions(discrete)</td>
<td></td>
</tr>
<tr>
<td>f / kHz</td>
<td>500</td>
<td>Switching frequency</td>
</tr>
<tr>
<td>( w_{IR} / \text{mm} )</td>
<td>6</td>
<td>Winding width on the HV side</td>
</tr>
<tr>
<td>( w_{LV} / \text{mm} )</td>
<td>4.25</td>
<td>Winding width on the LV side</td>
</tr>
<tr>
<td>( h )</td>
<td>2oz (0.07\text{mm})</td>
<td>Winding thickness</td>
</tr>
<tr>
<td>( \delta / \text{mm} )</td>
<td>0.5</td>
<td>Distance between any two arbitrary turns</td>
</tr>
<tr>
<td>( \sigma / \text{mm} )</td>
<td>0.254</td>
<td>Space between the core and turns</td>
</tr>
<tr>
<td>( Tr-P,A,B,C )</td>
<td>8:3:3:3</td>
<td>Number of turns</td>
</tr>
</tbody>
</table>

TABLE II
SIMULATION RESULT OF THE WINDING LOSS

<table>
<thead>
<tr>
<th></th>
<th>Integrated three-phase transformer</th>
<th>Discrete three-phase transformer</th>
</tr>
</thead>
<tbody>
<tr>
<td>Winding loss / W</td>
<td>1.9125</td>
<td>2.5065</td>
</tr>
</tbody>
</table>

Fig. 6. Picture of the PPDAB converter hardware prototype

TABLE III
SPECIFICATIONS OF THE PROTOTYPE

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{\text{in,nom}} )</td>
<td>400V</td>
<td>Nominal DC input voltage</td>
</tr>
<tr>
<td>( V_{\text{out,nom}} )</td>
<td>50V</td>
<td>Nominal DC output voltage</td>
</tr>
<tr>
<td>( P_r )</td>
<td>1kW</td>
<td>Rated power</td>
</tr>
<tr>
<td>( f_s )</td>
<td>500kHz</td>
<td>Switching frequency</td>
</tr>
<tr>
<td>( Tr-P,A,B,C )</td>
<td>8:3:3:3</td>
<td>Number of turns</td>
</tr>
<tr>
<td>Transformer</td>
<td>ML91S</td>
<td>Material</td>
</tr>
<tr>
<td>( L_{\text{in}} )</td>
<td>22.0213\mu H</td>
<td>Total leakage inductance re-ferred to the HV side</td>
</tr>
<tr>
<td>( \varphi )</td>
<td>10%</td>
<td>Phase shift between the HV side and LV side</td>
</tr>
<tr>
<td>Digital Contoller</td>
<td>TMS320F28379D</td>
<td>Digital signal processing</td>
</tr>
<tr>
<td>Power semiconductor</td>
<td>GS66504B</td>
<td>Switches on the HV side</td>
</tr>
<tr>
<td>Power semiconductor</td>
<td>EPC2021</td>
<td>Switches on the LV side</td>
</tr>
</tbody>
</table>

IV. EXPERIMENTAL RESULTS

In order to verify the proposed converter and its associated magnetic integration, a 1kW three-phase PPDAB converter with the newly integrated three-phase transformers was constructed and tested. The photo of the built prototype is shown in Fig.6 and the prototype specifications are summarized in Table III.

Two winding techniques i.e. wire-wound and print circuit board (PCB) winding are tested and compared for the proposed magnetic structure. Fig.7 shows the wire-wound transformer and its windings arrangement, and Fig.8 shows the solution with PCB windings and its windings arrangement. The transformer in Fig. 8 has a lower leakage inductance due to the windings interleaving.
However, the large surface area of the PCB windings increases the winding parasitic capacitance. The simulation and measured results of the transformers with these two different windings are listed in Table IV, where \( L_m \) is the magnetizing inductance of the windings on the HV side, \( L_k \) is the total leakage inductance referred to the HV side and \( C_p \) is the parasitic capacitance of the windings on the HV side. The equivalent circuit of the HV side is shown in Fig. 9. \( L_n \) is the inductance of a wire which is placed in series in the circuit path for ease on current probing.

The waveform of the three-phase PPDAB converter with the planar transformer with PCB windings working at 200 kHz is shown in Fig. 10. The waveform is labeled according to Fig. 1.

It shows that after S1 and S3 turn off, the \( L_w \) firstly resonates with output capacitance \( C_{\text{oss}} \) of the semiconductor devices on the HV side. The resonant frequency \( f_{\text{res 1}} \) can be calculated,

\[
f_{\text{res 1}} = \frac{1}{2\pi\sqrt{L_{\text{w}}C_{\text{oss}}}} \tag{16}
\]

After S2 and S4 turn on, the LCL form by \( L_{\text{w}}, C_p, \) and \( L_k \) results in a high amplitude current resonance and causes higher conduction loss of the primary devices.

The resonant frequency \( f_{\text{res 2}} \) can be expressed,

\[
f_{\text{res 2}} = \frac{1}{2\pi\sqrt{\frac{L_{\text{w}}L_k}{L_{\text{w}}+L_k}C_p}} \tag{17}
\]

Finally, the calculation results of the resonant frequency are listed in Table V.
It can be seen that the measured $f_{res}$ matches the calculated result, which verifies the resonance is because of LCL resonance of $C_p$, $L_k$, and $L_w$. However, there are still errors on the calculated $f_{res}$ and it will be studied further in the future work.

Therefore, in the final test, a wire-wound transformer shown in Fig. 7(a) is employed in the prototype. Fig. 11 provides the thermal picture of the proposed three-phase PPDAB converter working at 1kW, 400V/50V. The temperature of the devices in the HV side reaches 72°C, which is still within the safe temperature range. Fig. 12 gives the experimental waveforms of the proposed three-phase PPDAB converter operating at 1kW, 400V/50V. The waveforms are labeled according to Fig. 1. The proposed three-phase PPDAB converter has an efficiency of 96.72% at the rated power.

V. CONCLUSIONS

In this paper, a new integrated three-phase transformer is proposed for the PPDAB converter. Compared to the discrete three-phase transformer, the proposed integrated one has a lower winding loss. In order to ensure the leakage inductor is large enough to serve as the ac inductor in the PPDAB converter, the FEA method is used to design the windings structure of the proposed transformer. From the experimental results, we are able to verify the theoretical analysis and can see the three high-frequency ac currents in the paralleled branches are well balanced. Finally, the efficiency is 96.72% at the rated power. Modeling the leakage inductance of the proposed integrated three-phase transformer and optimizing the PCB windings to reduce the parasitic capacitance will be carried out in the future work.
REFERENCES


