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Electrical characterization of nanowires combined with in-situ TEM imaging

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We have developed unique microfabricated nanowire growth systems that enable epitaxial vapor phase (VPE) nanowire growth on monocrystalline Si cantilever substrates as shown in Fig 1 A). The chip systems enable both direct in-situ TEM observation of growth processes and electrical characterization of nanowires once they have formed bridges between cantilevers^{1, 2}.

Here we electrically characterize silicon nanowires grown and electrically connected at their two ends in-situ in the TEM at room temperature and elevated temperatures. We observe that the nanowires, which are oxide-free directly after growth, have non-linear current-voltage characteristics, Fig 1 B). We further study the effects of surface modification on the electrical properties of the nanowire by oxidizing the nanowire surface in several stages, recording images at each stage. These observations show how the repeated oxidation decreases the conductivity. Finally, we present new chip designs for characterizing electrical and optical properties of III-V nanowires at the III-V ETEM at Lund University.

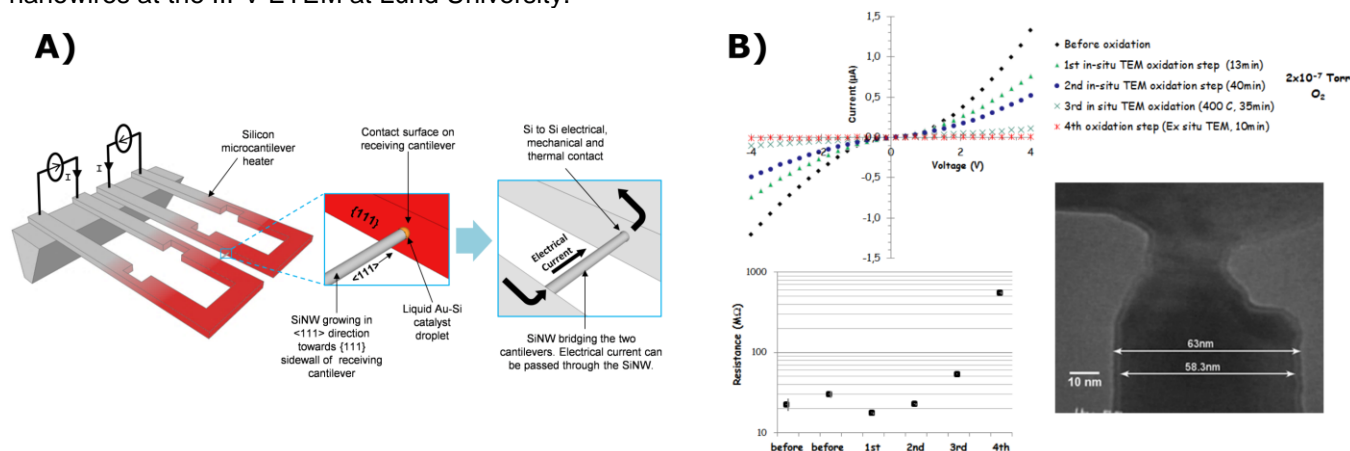


Figure 1. (A) Schematics of experimental setup used for in-situ growth and electrical characterization of nanowires. (B) IV data and resistance of nanowire shown in TEM image at different surface oxidation steps.

References

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³ The measurements were performed at IBM T. J. Watson Research Center