Ultrahigh-efficiency apodized grating coupler using fully etched photonic crystals

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Silicon-on-insulator (SOI) is considered as one of the most promising platforms for photonic integrated circuits thanks to its complementary metal-oxide semiconductor (CMOS) compatible fabrication technology [1]. In addition, the high refractive index contrast enables compact sizes for SOI devices. However, it also introduces a large mode mismatch between SOI nano waveguides and standard single mode fibers (SSMFs), making it very challenging to couple light between them efficiently. One method is to use a silicon inverse nano taper covered by a medium index contrast waveguide (such as polymer or silicon nitride) as a spot size converter [2,3]. However, this approach requires extra fabrication steps for the medium index contrast waveguide, and a lensed fiber is normally needed to reduce the mode mismatch. Another efficient solution is to use grating couplers. By properly designing the period and duty cycle of a diffraction grating structure implemented in a silicon waveguide, light guided in the waveguide can be coupled to an SSMF at a proper angle [4-10]. The grating coupler solution offers the great convenience of avoiding cleaving the sample, making wafer-scale testing possible. Normally the grating coupler is designed as uniform type with shallow etched slots to introduce a proper scattering strength [4-6]. However, the exponentially decaying field profiles of light diffracted from those gratings result in intrinsic mode mismatches to SSMFs with Gaussian-like field profiles, and the coupling efficiency is also sensitive to the etching depth [4]. In order to decrease such mode mismatches, shallowly etched apodized gratings have been proposed and demonstrated [7,8], at the expense of extra processing steps. To simultaneously simplify the fabrication and avoid the etching depth sensitivity, fully etched apodized gratings are preferred, so that silicon nanowires and the grating coupler can be fabricated in the same process. A uniform grating coupler based on fully etched photonic crystals (PhCs) has been demonstrated [9], exhibiting a coupling efficiency of 42%. A fully etched apodized grating coupler using square holes have been realized [10] to reduce the mode mismatch, showing, however, only a slightly improved coupling loss of −3.6 dB (44% coupling efficiency).

In this paper, we present an efficient method to design an ultrahigh-efficiency apodized grating coupler. Using this method, a fully etched apodized grating coupler is designed and demonstrated on the SOI platform using PhCs to engineer the refractive index of each scattering slot. An ultralow coupling loss of −1.74 dB (67% coupling efficiency) with a 3 dB bandwidth of 60 nm is experimentally measured. © 2013 Optical Society of America

We present an efficient method to design apodized grating couplers with Gaussian output profiles for efficient coupling between standard single mode fibers and silicon chips. An apodized grating coupler using fully etched photonic crystal holes on the silicon-on-insulator platform is designed, and fabricated in a single step of lithography and etching. An ultralow coupling loss of −1.74 dB (67% coupling efficiency) with a 3 dB bandwidth of 60 nm is experimentally measured. © 2013 Optical Society of America

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Silicon-on-insulator (SOI) is considered as one of the most promising platforms for photonic integrated circuits thanks to its complementary metal-oxide semiconductor (CMOS) compatible fabrication technology [1]. In addition, the high refractive index contrast enables compact sizes for SOI devices. However, it also introduces a large mode mismatch between SOI nano waveguides and standard single mode fibers (SSMFs), making it very challenging to couple light between them efficiently. One method is to use a silicon inverse nano taper covered by a medium index contrast waveguide (such as polymer or silicon nitride) as a spot size converter [2,3]. However, this approach requires extra fabrication steps for the medium index contrast waveguide, and a lensed fiber is normally needed to reduce the mode mismatch. Another efficient solution is to use grating couplers. By properly designing the period and duty cycle of a diffraction grating structure implemented in a silicon waveguide, light guided in the waveguide can be coupled to an SSMF at a proper angle [4-10]. The grating coupler solution offers the great convenience of avoiding cleaving the sample, making wafer-scale testing possible. Normally the grating coupler is designed as uniform type with shallow etched slots to introduce a proper scattering strength [4-6]. However, the exponentially decaying field profiles of light diffracted from those gratings result in intrinsic mode mismatches to SSMFs with Gaussian-like field profiles, and the coupling efficiency is also sensitive to the etching depth [4]. In order to decrease such mode mismatches, shallowly etched apodized gratings have been proposed and demonstrated [7,8], at the expense of extra processing steps. To simultaneously simplify the fabrication and avoid the etching depth sensitivity, fully etched apodized gratings are preferred, so that silicon nanowires and the grating coupler can be fabricated in the same process. A uniform grating coupler based on fully etched photonic crystals (PhCs) has been demonstrated [9], exhibiting a coupling efficiency of 42%. A fully etched apodized grating coupler using square holes have been realized [10] to reduce the mode mismatch, showing, however, only a slightly improved coupling loss of −3.6 dB (44% coupling efficiency).

In this paper, we present an efficient method to design an ultrahigh-efficiency apodized grating coupler. Using this method, a fully etched apodized grating coupler is designed and demonstrated on the SOI platform using PhCs to engineer the refractive index of each scattering slot. An ultralow coupling loss of −1.74 dB (67% coupling efficiency) with a 3 dB bandwidth of 60 nm is demonstrated. The fabrication tolerance is also investigated, showing a 10 nm peak coupling wavelength shift for 4 nm hole size change.

The proposed method comprises four steps. The first step is to use a two-dimensional (2D) model to analyze the scattering cells of an apodized grating coupler (see Fig. 1) considering the buried silicon oxide (BOX) layer to be infinitely thick. As an example, a scattering angle \( \theta = 10^\circ \) is chosen and the grating coupler is designed to work on the transverse electric (TE) mode of the silicon waveguide. A 250 nm top silicon layer is considered, and air is used as upper cladding. For each scattering cell of length \( l_i \), where the index \( i \) denotes the number of the concerned cell, an artificial material slot with index \( n_i \), and a slot width \( d_i \) is introduced, resulting in a power leakage factor \( 2\alpha_i \). In the design, \( d_i \) is fixed to be \( d_0 = 345 \) nm (inset of Fig. 2) so that \( n_i \) can be tuned by only changing the PhC hole size, as described in step 4. The Bloch constant, which determines the effective refractive index \( n_{eff} \) and power leakage factor \( 2\alpha \), is calculated by the 2D eigenmode expansion method (EME) [11]. The scattering wavelength \( \lambda \) is then given by

![Fig. 1. 2D analytical model of the apodized grating coupler; SMF, single mode fiber.](http://dx.doi.org/10.1364/OL.38.002732)
Fig. 2. Calculated required refractive index \( n_i \) of the artificial material of a scattering cell and the corresponding power leakage factor as a function of the scattering cell length \( l_s \).

\[
\lambda = l(n_{\text{eff}} - n_{\text{up}} \sin \theta),
\]

where \( n_{\text{up}} \) is the refractive index of the upper cladding (air). For a given \( l_s \), the required refractive index \( n_i \) of the artificial material is found so that the scattering cell has a scattering angle \( \theta = 10^\circ \) at 1550 nm. The corresponding \( 2\alpha_i \) is also calculated, as shown in Fig. 2. This provides a guideline to synthesize any leakage factor distribution for a grating coupler.

The second step is to synthesize the required leakage factor distribution according to the results obtained in step 1. In order to obtain a Gaussian output profile with beam diameter of 10.4 \( \mu \)m, corresponding to that of an SSMF, the power leakage factor distribution \( 2\alpha(z) \) of an apodized grating coupler should satisfy the following relation [8]:

\[
2\alpha(z) = G^2(z)/\left[1 - \int_0^z G^2(z)dz\right].
\]

Figure 3(a) shows the required power leakage factor, calculated according to Eq. (2), in order for a grating coupler to scatter a Gaussian beam centered at \( z = 10 \mu \)m. The grating parameters \( l_s \) and \( n_i \) are then designed according to the target power leakage profile [see Fig. 3(a)] and the calculated results of Fig. 2, as shown in Fig. 3(b), resulting in a good match with the target power leakage factor \( 2\alpha(z) \) and the output Gaussian profile \( G(z) \).

The third step is to optimize the BOX thickness with the designed grating parameters given by Fig. 3(b). The coupling efficiency is calculated by the 2D EME method as a function of the BOX thickness, as shown in Fig. 4(a). One can find that the coupling efficiency depends periodically on the BOX thickness, and reaches its maximum with a BOX layer of around 1.1 \( \mu \)m. Figure 4(b) shows the calculated field distribution of the designed SOI grating coupler with a 1 \( \mu \)m BOX layer when TE light is injected into the grating coupler, confirming that a Gaussian beam is vertically coupled out at \( 10^\circ \), as designed.

The final step is to engineer the refractive index of each artificial material slot using fully etched PhCs holes. In particular, a triangular lattice is selected in our design [9]. The effective index \( n_{2D} \) of a 2D triangular lattice PhC can be estimated as

\[
\frac{n_{2D}^2}{n_b^2} = \frac{n_c^2(1+f) + n_a^2(1-f)}{n_c^2(1-f) + n_a^2(1+f)},
\]

where \( n_c \) and \( n_b \) are the refractive indices of the cylindrical inclusions (air) and host medium (silicon), respectively [12]. \( f = 3\sqrt{3}D^2/16d_0^2 \) is the volume filling fraction of the cylindrical inclusions. The hole diameter \( D \) of each artificial material slot is then determined by the required \( n_i(z) \) distribution and Eq. (2). The final grating design is presented in Fig. 5(a). Figure 5(b) shows the calculated coupling efficiency of the designed grating coupler (1 \( \mu \)m BOX layer) as a function of wavelength.
A lowest coupling loss of $-1.8 \, \text{dB}$ (66% coupling efficiency) is predicted, which is better than a calculated $-3.1 \, \text{dB}$ coupling loss for a uniform type grating with the same artificial slot width $d_0$ and an optimized $2\alpha$ of 0.26 $\mu \text{m}^{-1}$ [13]. In addition, a hole size change of $\Delta D = \pm 4 \, \text{nm}$ results in a $\pm 10 \, \text{nm}$ peak coupling wavelength shift. Considering that the minimum hole diameter ($D_{\text{min}}$) of our design is around 60 nm, which could not be realized by fabrication methods such as deep ultraviolet (DUV) lithography, the impact of restricting the feasible hole diameter to above 100 nm is investigated, as shown in Fig. 5(a). In this case, the corresponding cell length should also be adjusted according to Fig. 2, since the effective refractive index $n_i$ of the slots with designed air hole diameters below 100 nm has been changed. One can find that only 0.2 dB degradation in maximum coupling loss is achieved, as shown in Fig. 5(b), indicating that the design is feasible with most fabrication methods.

In order to validate our design method, a grating coupler with the designed parameters was fabricated on a SOI wafer with top silicon thickness of 250 nm and BOX of 1 $\mu \text{m}$. A single step of standard E-beam lithography based nano fabrication processes [14] were used to fabricate the fully etched silicon wave-guides and grating couplers, simultaneously. The 450 nm-wide and 700 $\mu \text{m}$-long straight waveguide is laterally tapered with 500 $\mu \text{m}$ tapering lengths at both ends to the grating couplers (20 $\mu \text{m}$ long and 12 $\mu \text{m}$ wide). Several PhC columns with apodized hole sizes are introduced as the artificial material, as shown in Fig. 6(a). Figure 6(b) shows the measured coupling loss to an SSMF mounted at 10° as a function of wavelength. The coupling loss is calculated as $(\eta_0 - \eta_s)/2$, where $\eta_0$ is the grating-to-grating transmission and $\eta_s$ is the loss of the straight wave-guide with a typical propagation loss of 4.3 $\text{dB/cm}$ [15]. A coupling loss of only $-1.74 \, \text{dB}$ (67% coupling efficiency) with a 3 $\text{dB}$ bandwidth of 60 nm are measured. Grating couplers with a $\pm 4 \, \text{nm}$ hole diameter deviation are also characterized, showing $\pm 10 \, \text{nm}$ peak coupling wavelength shift, in good agreement with the calculations. Such a peak coupling wavelength shift can be compensated by adjusting the coupling angle [4]. To the best of our knowledge, this is the highest reported coupling efficiency for fully etched grating couplers. Further coupling loss improvement could be achieved with backside metal mirrors [16].

We have presented an efficient method to design an apodized grating coupler. An ultrahigh-efficiency fully etched apodized grating coupler based on PhCs has been designed and demonstrated, showing a low coupling loss of $-1.74 \, \text{dB}$ with a 3 $\text{dB}$ bandwidth of 60 nm. The fabrication tolerance of the designed grating coupler was also investigated both theoretically and experimentally.

References